

revision 7.48

WARNING:

Attempting to change the value of some components during a simulation will not interrupt the execution but its outcome may become *silently misleading*

Depending on the topology of the circuit, some **flux** and/or **charge** may have to be partially redistributed on some other components

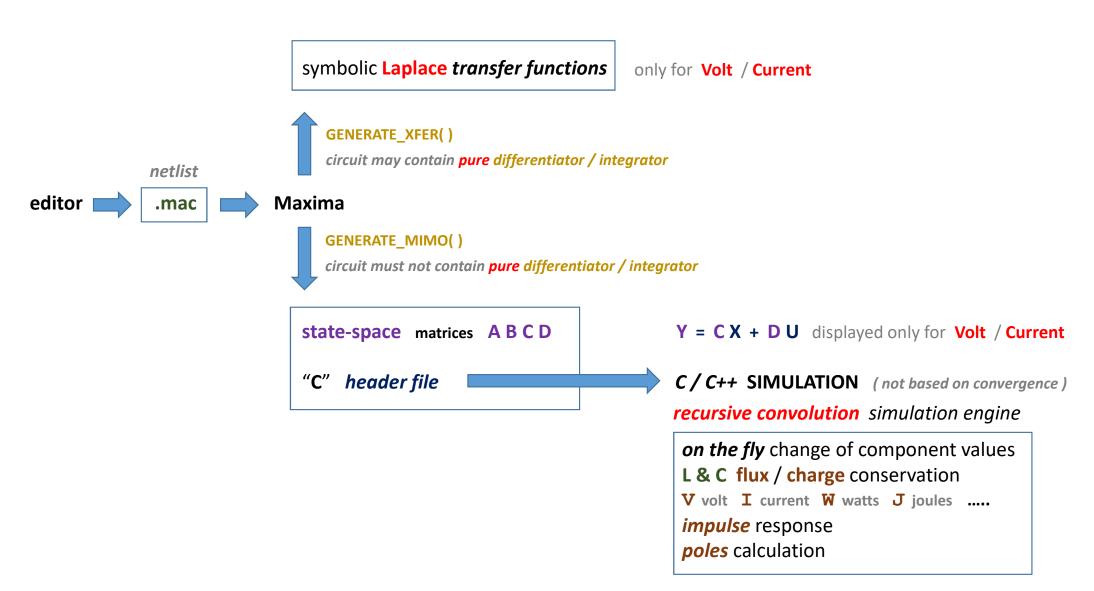
(trivial example: changing the value of a capacitor in parallel with another one)

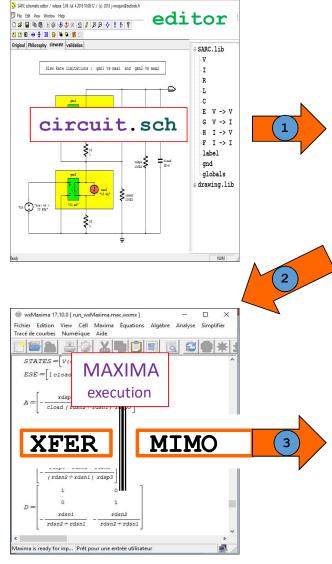
Therefore, simulating accurately component "percussions" requires some additional analysis, equations and processing

For Laplace transfer function (XFER / ILT) and Z transform (ZTRANS) such functionality has not been implemented and simulations may turn to be silently misleading

In summary,

only STATE model (MIMO) is able to simulate accurately "on the fly" component changes





```
SARC file viewer D:\JM\HOME\LAB\slewrate.mac
                                  netlist
xmldocument : "slewrate"
xmlpage : "slewrate"
xmldate : "Fri Jul 6 11:07:33 2018 generated with release 3.06";
SCHEMATIC ( "
Slew Rate limitations : gmn1 vs max1 and gmn2 vs max2
");
NETLIST (
            circuit.mac
        [ I , max2 , n03 , n04
                    , n02 , n03
        [ R , r1
                    , n04 , GND
             , r2
             , rdsn1 , out , n03
             , rdsn2 , n03 , GND
             , rdsp3 , out , GND
        [ C , cload , out , GND
        [ G , gmn1 , out , n02 , GND , n03 ] ,
        [ G , gmn2 , n03 , n04 , n01 , GND ]
```

```
#define MIMO slewrate A {\
    ((-1.0e+9*\overline{T}[0])-210\overline{T}[0]) \ (5.0e+8*\overline{T}[0]) \ (5.0e+8*\overline{T}[0]) \ (5.0e+8*\overline{T}[0]) \ (5.0e+9*\overline{T}[0]) \ (5.0e+8*\overline{T}[0]) \ (5.0e+9*\overline{T}[0]) \ (5.0e+14*\overline{T}[0]) \ (6.0e+9*\overline{T}[0]) \ (6.0e+9*\overline{T}[
```



A schematic XML file is called a "DOCUMENT" that can be

displayed on a resizable window

that may contain several "PAGEs"

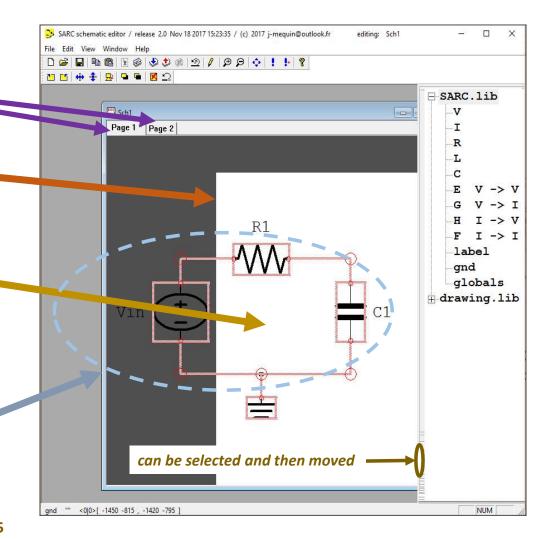
Each "PAGE" is a "container"

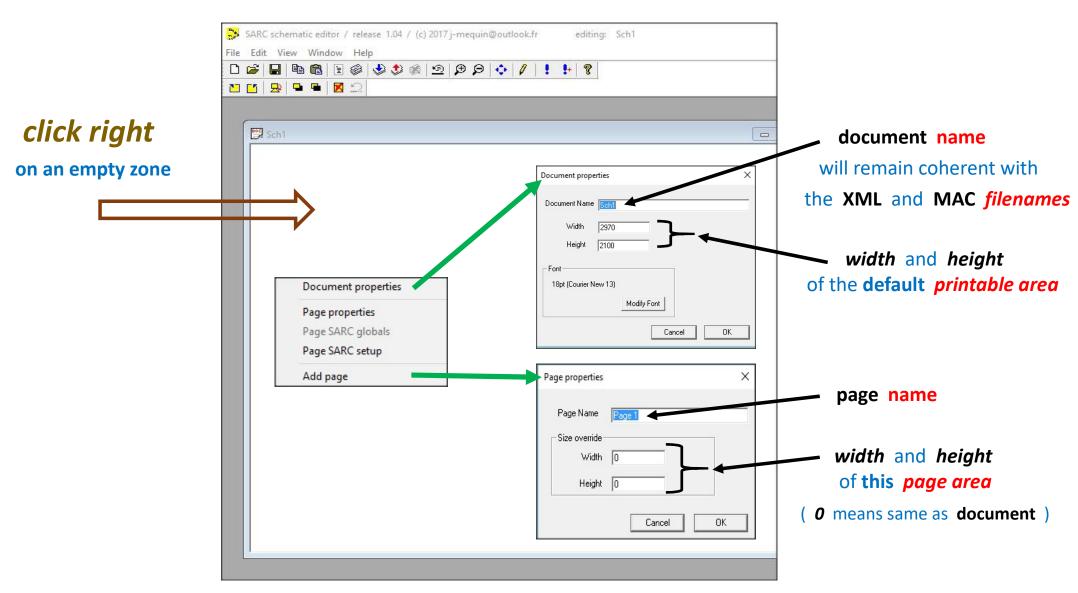
including the

available surface for the drawing

by default width = 2970 height = 2100 (thus landscape)

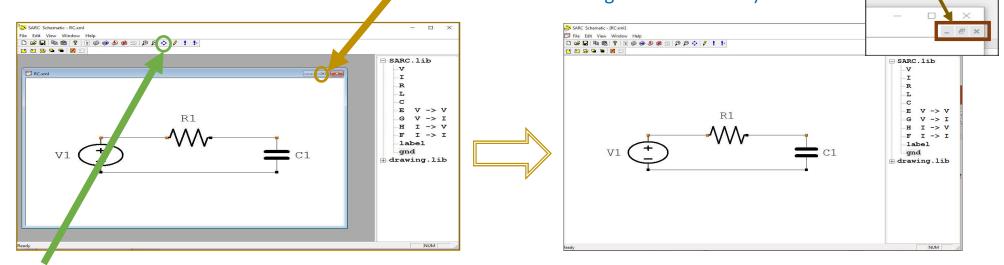
this whole schematic needs to be moved to the printable area





Several **XML** files can be active at the same time, allowing to *cut / paste* selected item(s) between them

A **single page** can be expanded to the full area by **clicking** (and then, can still be **minimized**, **restored**, **closed** or **closed** through the **File menu**)



The icon allows to rescale the schematic in order to fulfill the visible window area (= ZOOM to fit)

After positioning the mouse, the space bar centers the view without modifying the scale

After positioning the mouse, Page UP increases the zoom by 25%, Page Down decreases the zoom by 1/25%, around the mouse or Ctrl + or Ctrl -

The *mouse wheel* can be also used to control the zoom

Any object on the "Page" can be selected by clicking left on it

Once selected, the objects can be copied deleted pasted moved ctrl-C del key or ctrl-X ctrl-V click left

Objects can be **deselected** by clicking on an **empty area** or with the **ESCAPE** key

Holding Ctrl key allows to select all the objects that are inside a rectangular zone delimited with the mouse clicking left

Once selected, the area can be copied deleted pasted moved

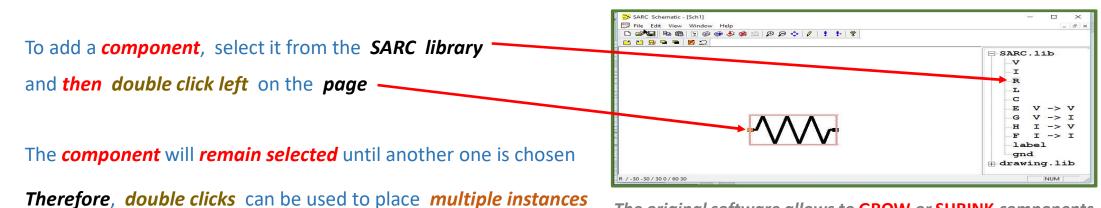
ctrl-C del key or ctrl-X ctrl-V holding ctrl key + click left

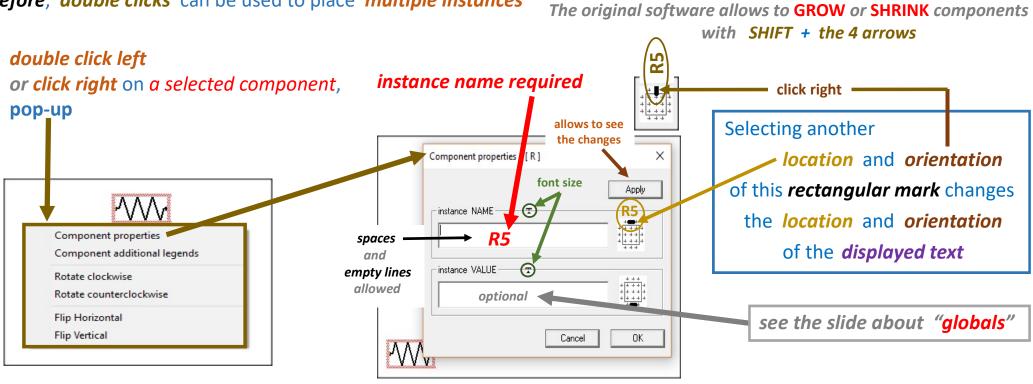
An area can be deselected by clicking elsewhere or with the ESCAPE key

Holding both Ctrl and Alt, and then moving the mouse clicked left, allows to move EVERYTHING within the page container

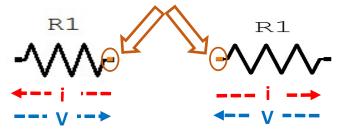
ctrl-Z	undo	undo last edit operation(s)	\Rightarrow	undo icon
ctrl-A	select	everything on the page	F2	check page for missing connection diagnostics
ctrl-C	сору	whatever is <i>selected</i> to the clipboard	F3	display net names (after netlist generation)
ctrl-V	paste	the contents of the clipboard	F4	playback all LIFO stack undo items

ctrl-F search an instance name (or a NET name) in the schematic





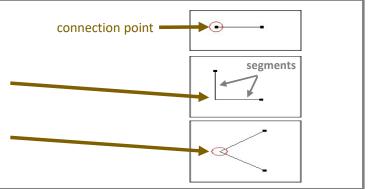
On each SARC component, an orange mark allows to identify, despite possible rotations, its SARC netlist pins order



Holding Alt key while *clicking mouse left* allows to *draw a* WIRE *segment*

While holding the **Alt key**, **toggling** the **mouse left** allows to **change direction**

Without holding the **Alt key**, *clicking* the *mouse left* allows to *drag* the *selected location* of an existing **WIRE**

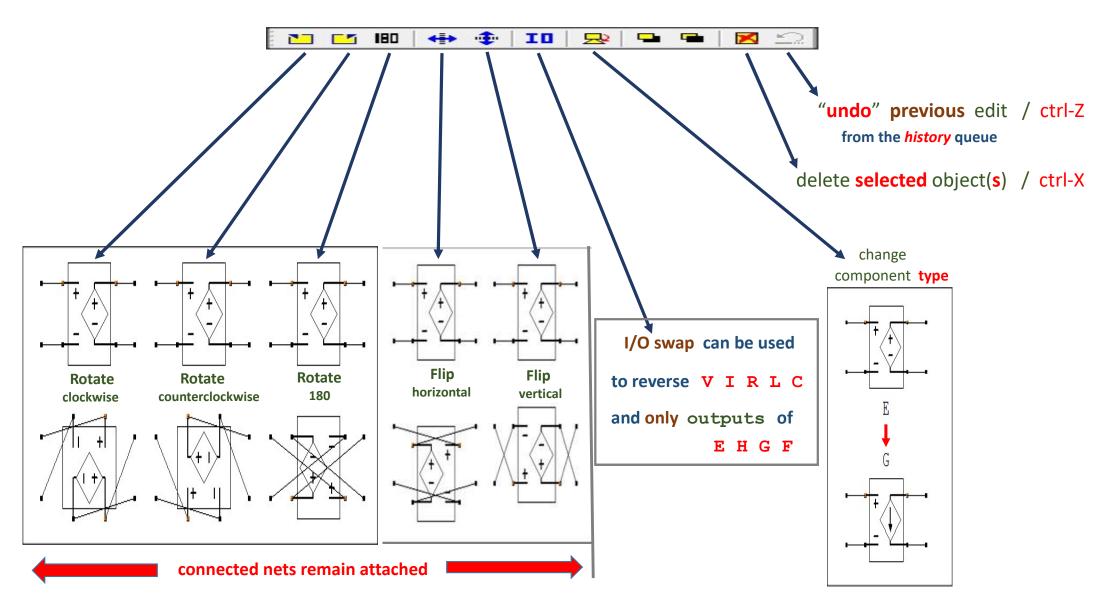


Pressing **F1** allows to make disappear **BOTH** the *orange marks* and the *connection points* (example, for nicer plot)

label Name

The SARC component "label" \(\subseteq \) can be used to associate a name to a NET (overriding its default "nxx") "Distant labels "sharing same name are wirelessly connected!!!

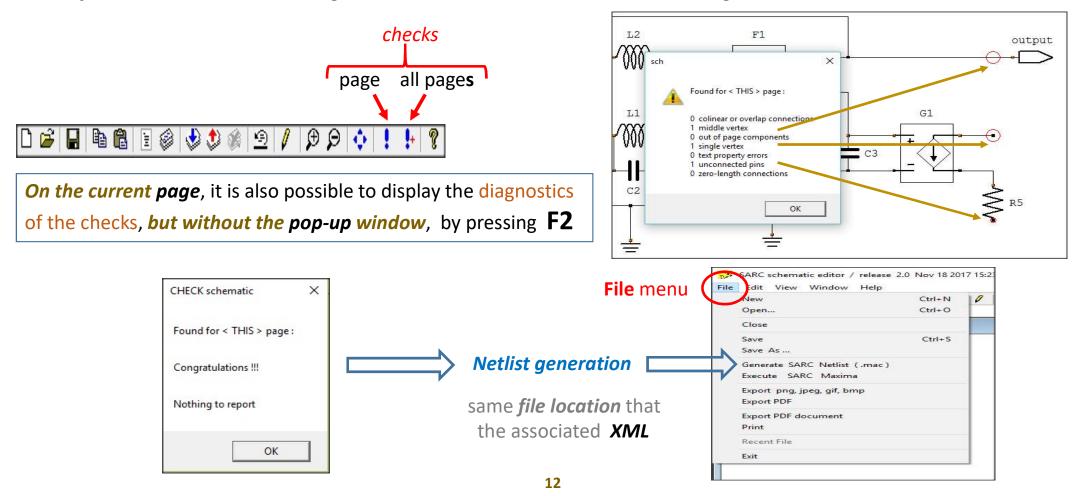
The objects "Title" or "Sticker" of the "drawing.lib" can be used to add some legends to the schematic

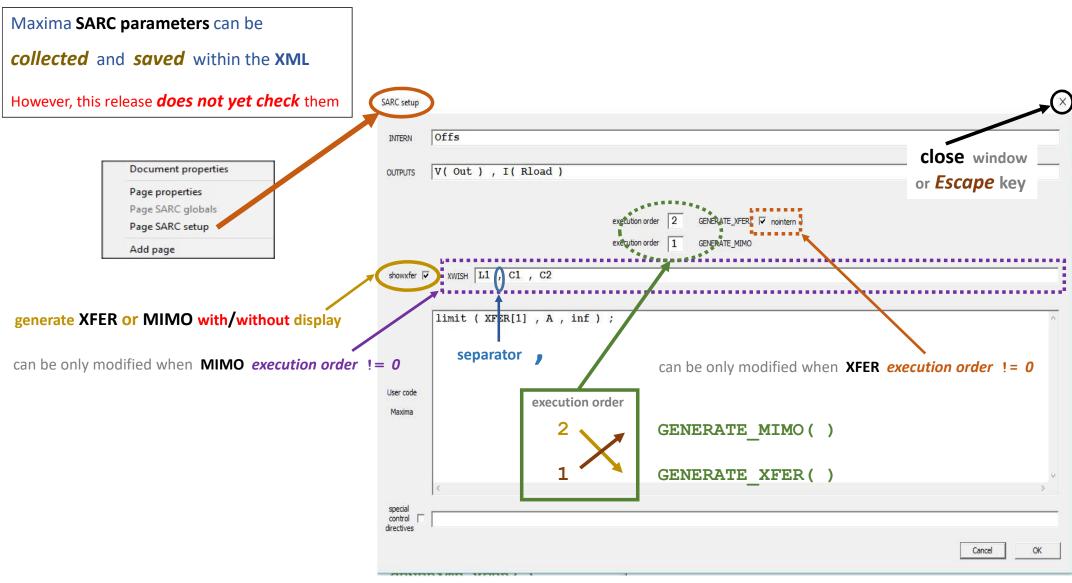


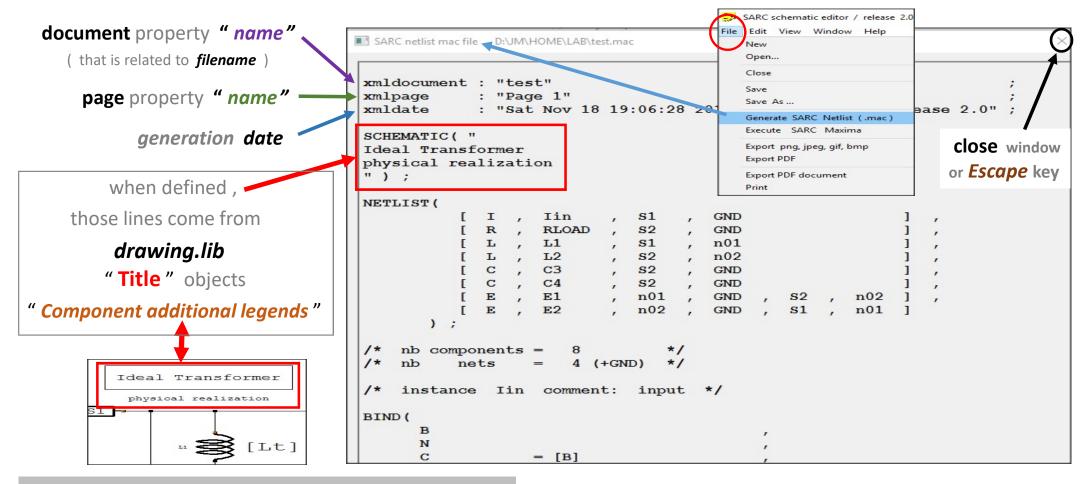
Before the *netlist generation* it is **mandatory** to perform some *sanity checks* on the schematic(s)

Those checks are almost the ones of the original software and are not as exhaustive as the SARC Maxima version

Therefore, even if a netlist is later generated, it can still contains some remaining issues







In case of several **Title** use « **Bring to front** » icon to select one of them

```
Escape-S can be used to view the current schematic ".sch" file

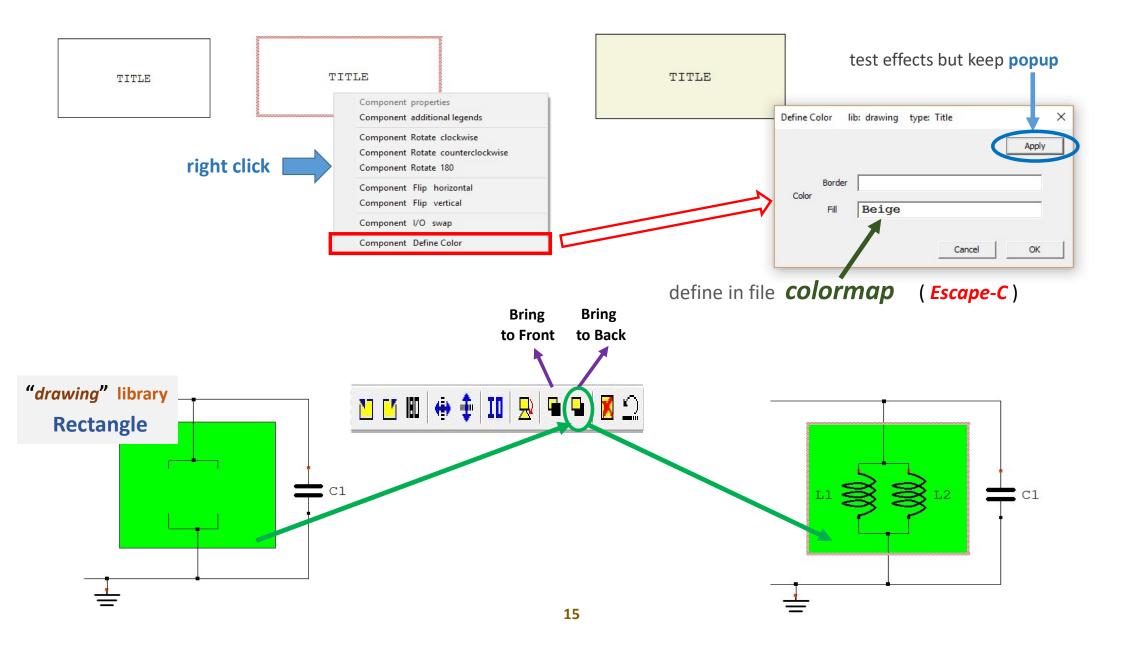
Escape-P can be used to view ( AFTER schematic Save / Save as ) the latest generated ".pdf" file

Escape-M can be used to view ( AFTER generate SARC Netlist ) the latest generated ".mac" file

Escape-H can be used to view ( AFTER GENERATE_MIMO( ) ) the latest generated ".hdr" file

Escape-Z can be used to view ( AFTER GENERATE Z( ) ) the latest generated ".hdr" file
```

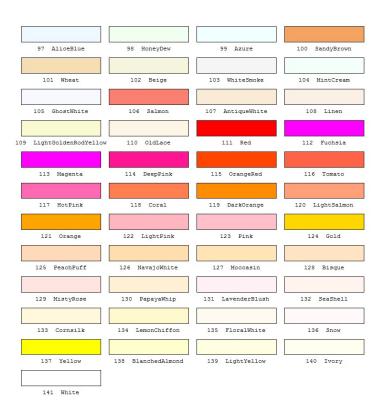
Escape-C can be used to view the **colormap**





49 LightSkyBlue 50 BlueViolet 51 DarkRed 52 DarkMagenta 54 YellowGreen 56 LightGreen 53 SaddleBrown 55 DarkSeaGreen 57 MediumPurple 58 DarkViolet 59 PaleGreen 60 DarkOrchid 61 Navyblue 62 Sienna 63 Brown 64 DarkGray 65 LightBlue 66 GreenYellow 67 PaleTurquoise 68 LightSteelBlue 69 PowderBlue 70 FireBrick 71 DarkGoldenRod 72 MediumOrchid 73 RosyBrown 74 DarkKhaki 75 Silver 76 MediumVioletRed 77 IndianRed 78 Peru 79 Chocolate 80 Tan 82 Thistle 83 Orchid 84 GoldenRod 81 LightGrey 85 PaleVioletRed 86 Crimson 87 Gainsboro 88 Plum 89 Burlywood 90 LightCyan 91 Lavender 92 DarkSalmon 93 Violet 94 PaleGoldenRod 95 LightCoral 96 Khaki

ESC-C



COLORS can be defined



by **index** or by **name** (case insensitive)

OUTPUT syntax examples:

```
SARC setup

INTERN

OUTPUTS V(Cx), V(Cy), I(Rx), I(Ry)
```

```
V(label)
                       label: netlist node
V( label1: label2 ) voltage between 2 labels (note: GND is a label)
V( component )
                     components: U I R L C and E G H F outputs
I( component )
                    components: U I R L C and E G H F outputs
W(component)
               components : U I R L C and E G H F outputs
cV( voltage control ) control side of E G
cI ( current control ) control side of H F
       note: the Y[] vector displayed by wxMAXIMA only contains V() cV() I() cI()
```

component parameters



string

« hello world » comments are ignored by Maxima



symbolic

3*gain k*sqrt(gain) 0.8 / gain substituted at the start of Maxima analysis

symbolic definitions must reference at least one variable

NOTE: special syntax

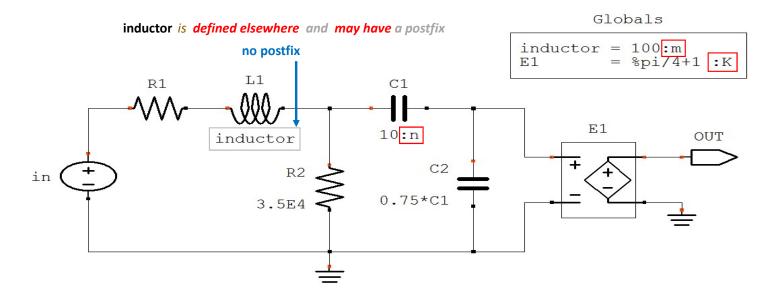
[1] [+1] [-1] (1 is the only accepted « value »)



numeric

0.4 12E-3 sqrt(%pi) substituted at the end of Maxima analysis

numeric definitions must not reference any variable



schematic instance *numeric values* can be *postfixed* with a multiplicative unit

```
:P peta 10^+15
:T tera 10^+12
:G giga 10^+9
:M mega 10^+6
:K kilo 10^+3
```

```
:m mili 10^-3
:u micro 10^-6
:n nano 10^-9
:p pico 10^-12
:f femto 10^-15
```

lowercase

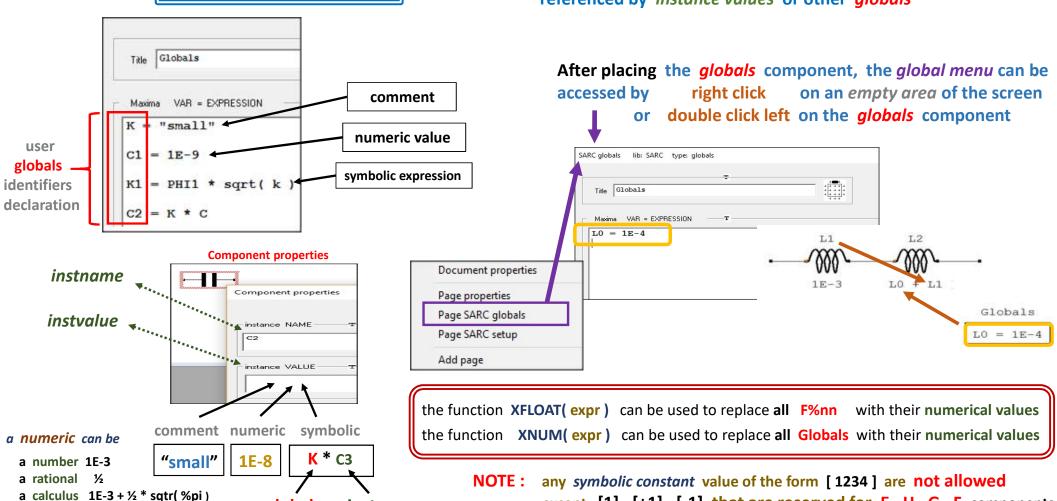


global or instname

not referencing instname or global

A special SARC component named « globals » can be instantiated on the page

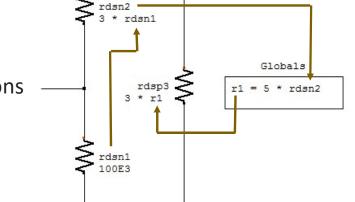
Its purpose is to define circuit global parameters referenced by instance values or other globals



20

except [1] [+1] [-1] that are reserved for E, H, G, F components



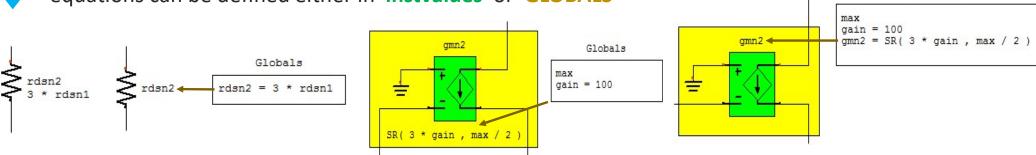


Globals

- instnames are by definition « declared » and « used »
- GLOBALS variables must be also « declared » and « used » (in instvalues or GLOBALS)

 R1 R1 = « load » R1 = 100 R2 = 2 * R1

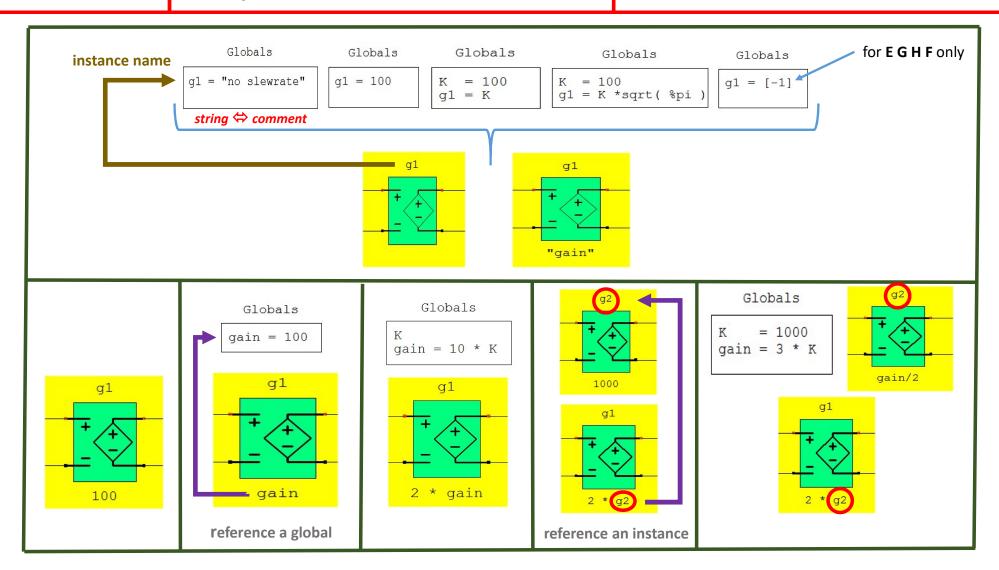
equations can be defined either in instvalues or GLOBALS

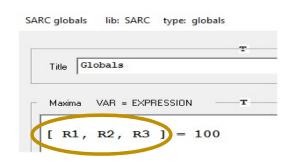


« xxx » ignored string

xxxx symbolic expressions substituted at build time

33.5 or 3E-8 **numeric** substituted at **run time**





declarations can be grouped

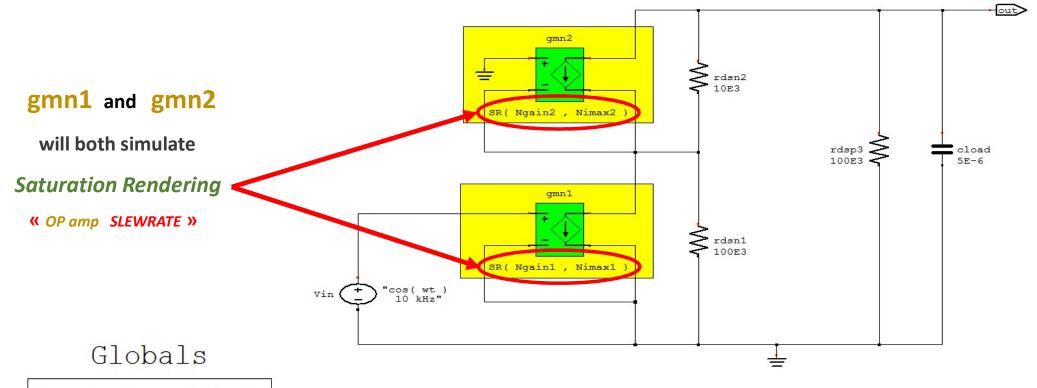
runtime directives can also be defined with **GLOBALS**

%rubmode	= true
%printpoles	= 3
%srtol	= 1E-3
%srlim	= 25
%phases	= [Phi1 , Phi2]
%zopen	= Gds
%zopen	= [Gds1,Gds2]
%zshort	= Rds
%zshort	= [Rds1 , Rds2]

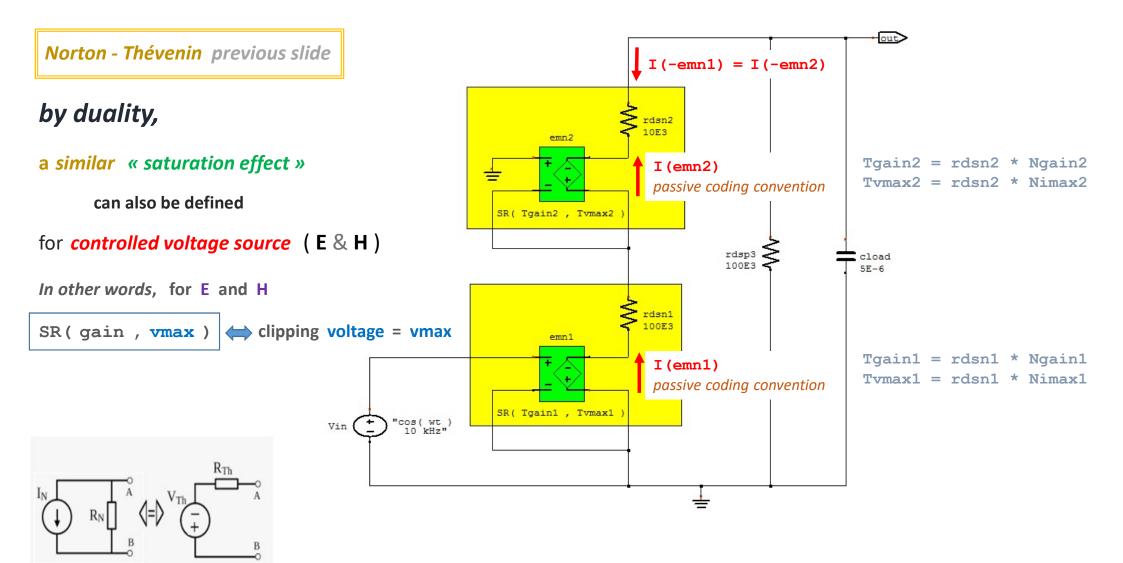
```
do not plot instantaneous percussion spikes (simulation remains unchanged) (default false)
report the computations of the poles due to the first 3 BOM changes (default 0)
saturation rendering computation tolerance (default 1E-4)
roughly, dividing %strol by 10 increases the number of iterations by +4
saturation rendering computation iteration limit (default 50)
mandatory for Z transform but can be declared for XFER / MIMO (no default)
component 'Gds' will be ignored by GENERATE_Z()

component 'Rds' will be shorted by GENERATE_Z()
```

In electronic, slew rate is defined as the maximum change of current or voltage per unit of time (max(di/dt) or max(dv/dt)) but, rather than this «slope control», SARC slew rate simulates the OP amp «clipping» effect on current sources



targetted tolerance = fabs((maxOUTPUT - currentOUTPUT) / maxOUTPUT)
max iterations

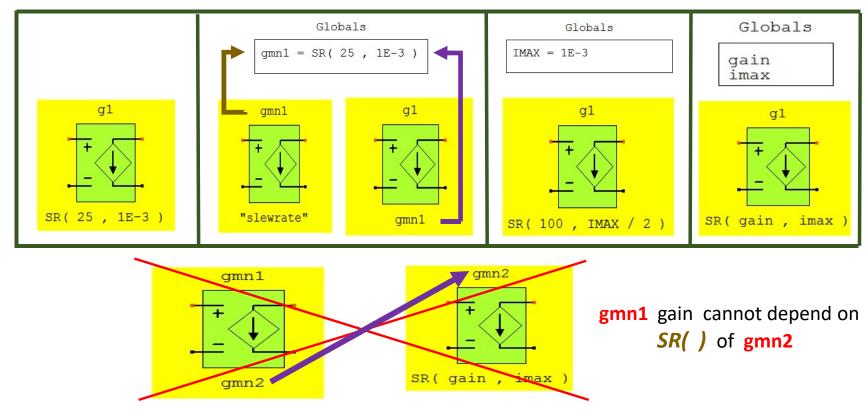


Thévenin

Norton

SR(gain, maxout) syntax

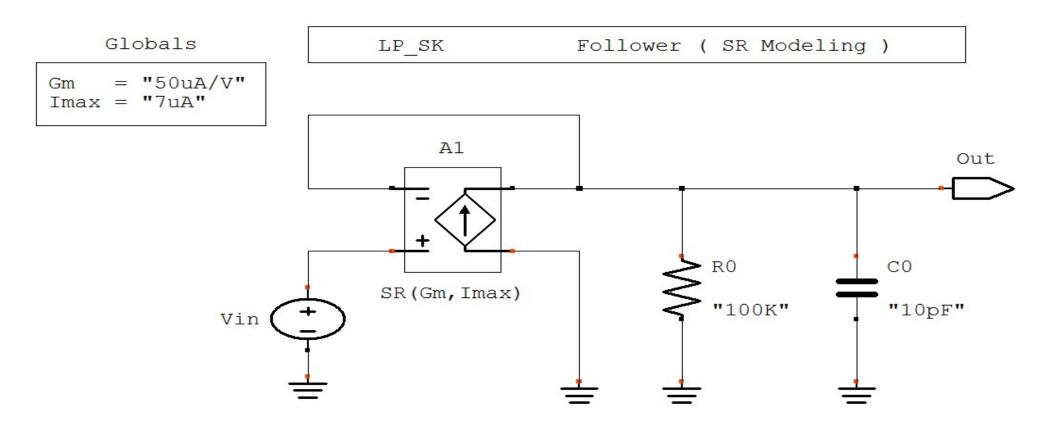
maxout | Imax for G and F | maxout = 0 → linear mode | The control of the contro



There is no need to have several schematics: GENERATE_XFER and GENERATE_Z ignore SR() declarations

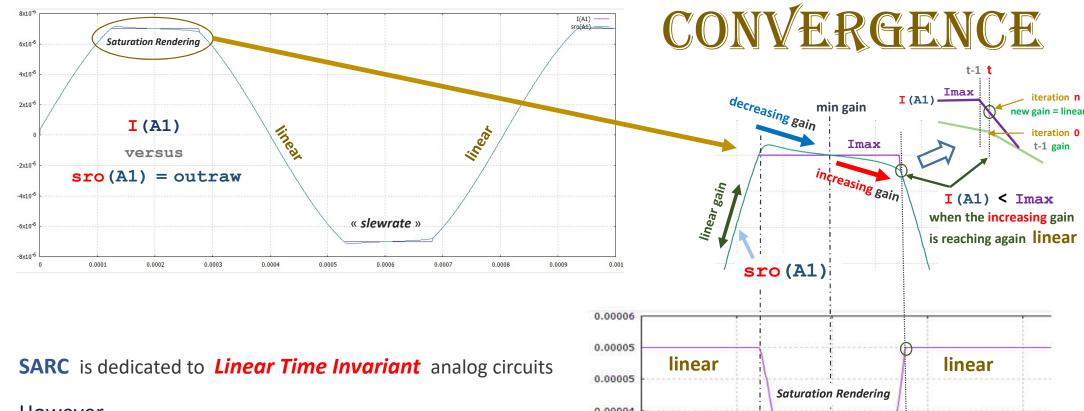
GENERATE_XFER and GENERATE_MIMO ignore %phases declarations

MIMO SR() simulations can be performed LINEAR by setting maxout = 0



3 special outputs can be plotted to observe the Saturation Rendering convergence

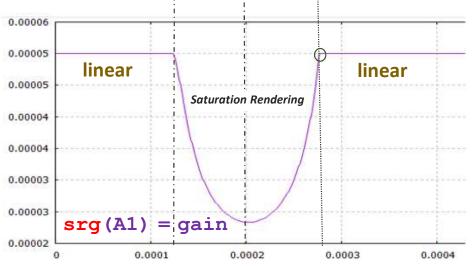
- srn (A1) number of recalculations
- sro(A1) raw output before relaxation
- srg(A1) effective reduced gain necessary to sustain max output



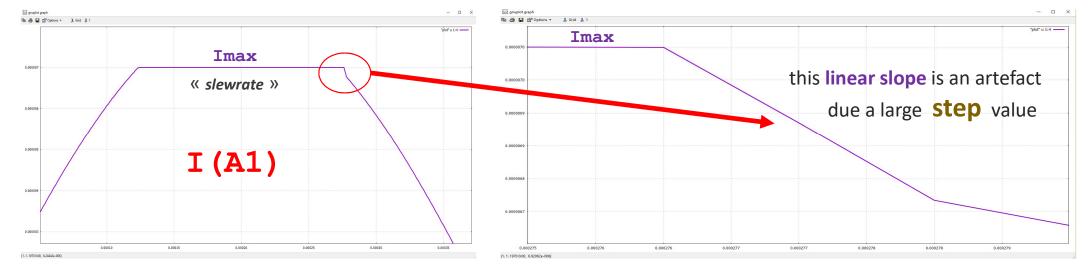
However,

SARC supports « *Saturation Rendering* » that is a **nonlinear behavior**

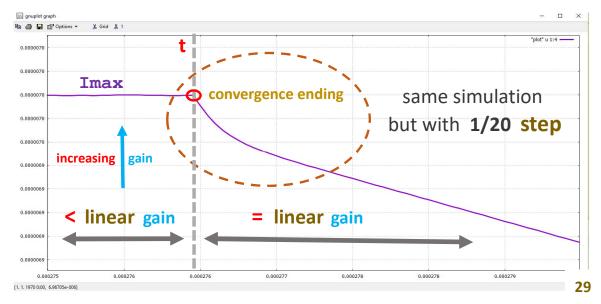
This can be only simulated through a *convergence process*

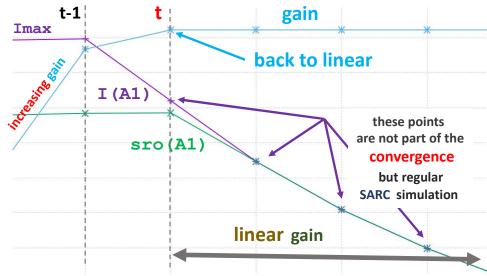


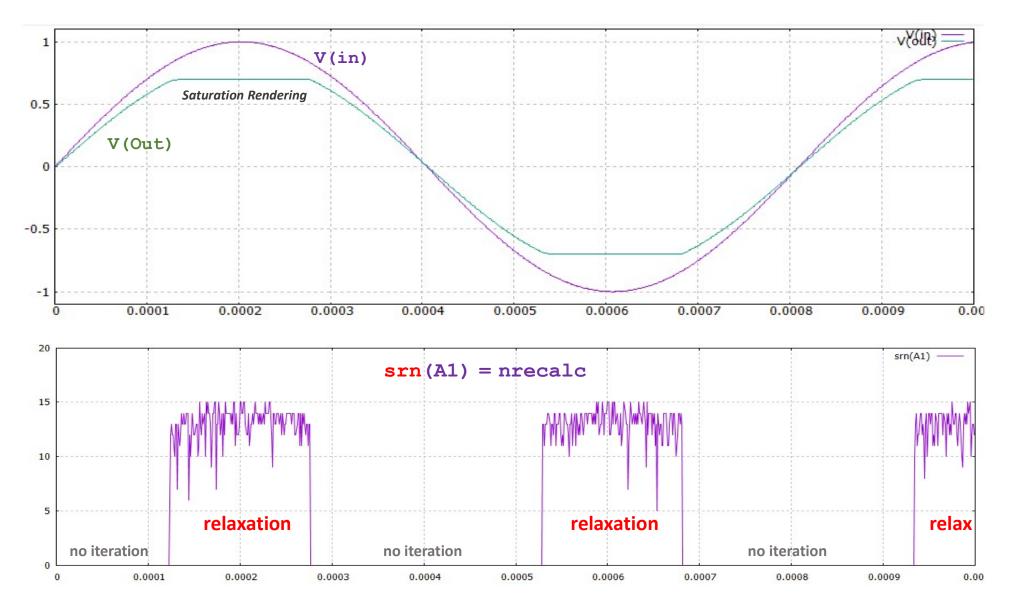
effective reduced gain necessary to sustain max output

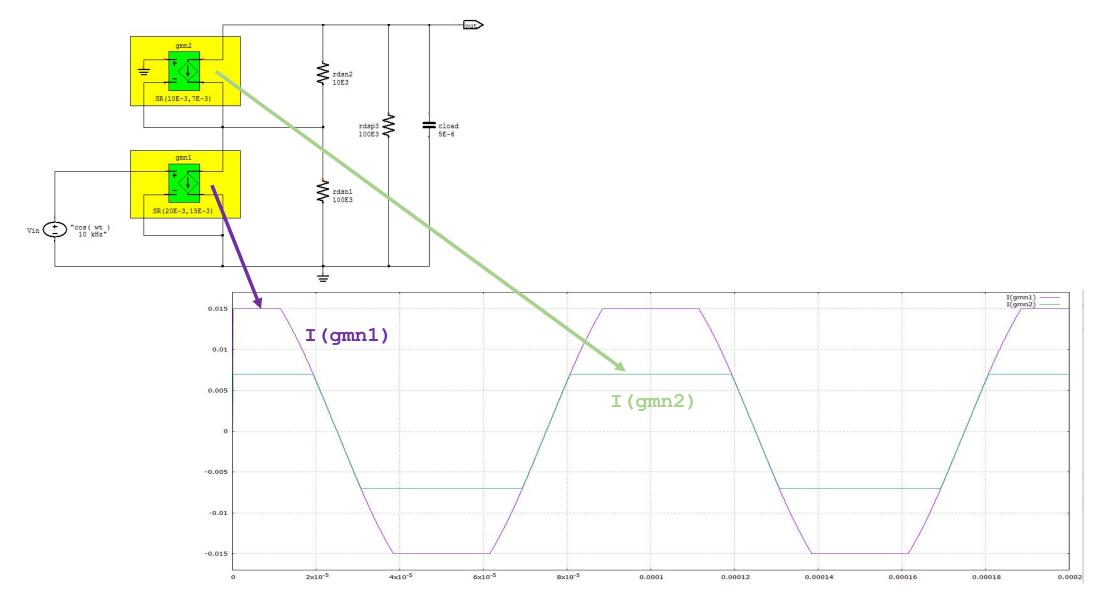


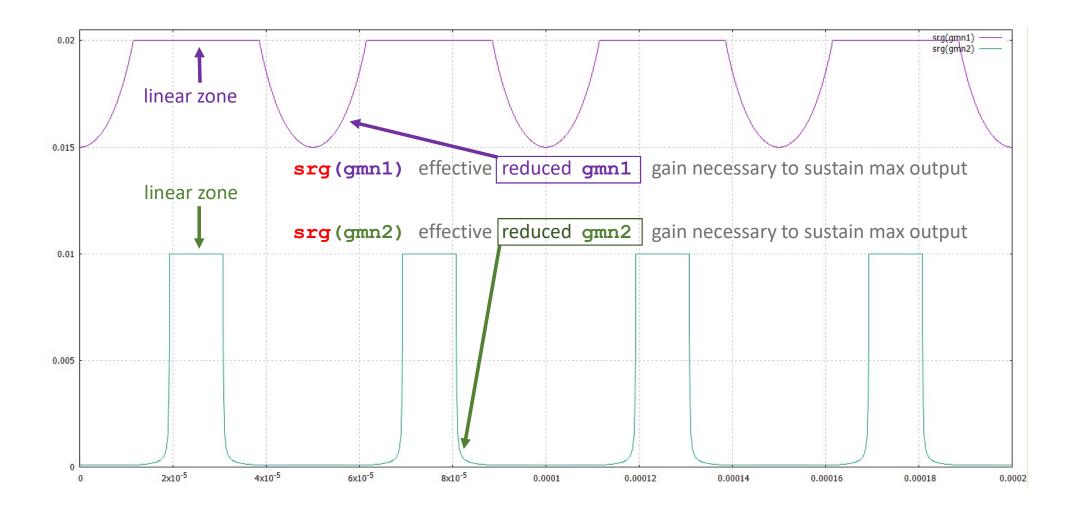
CONVERGENCE ENDING

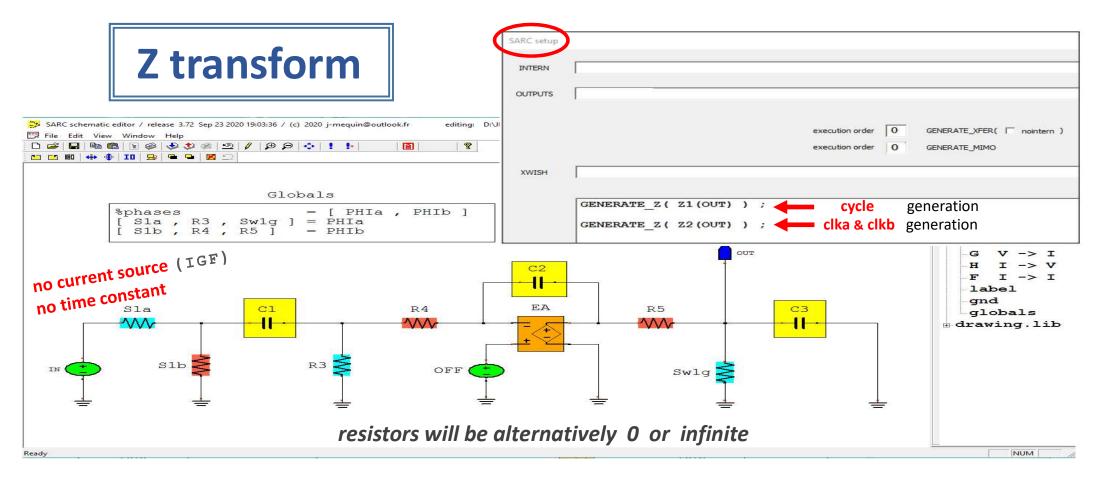








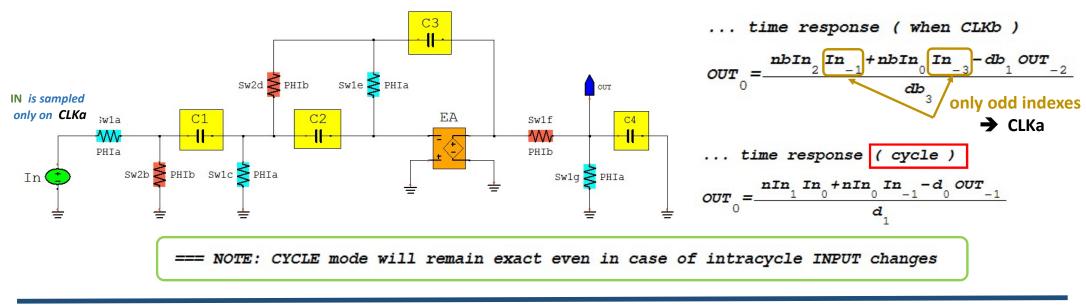


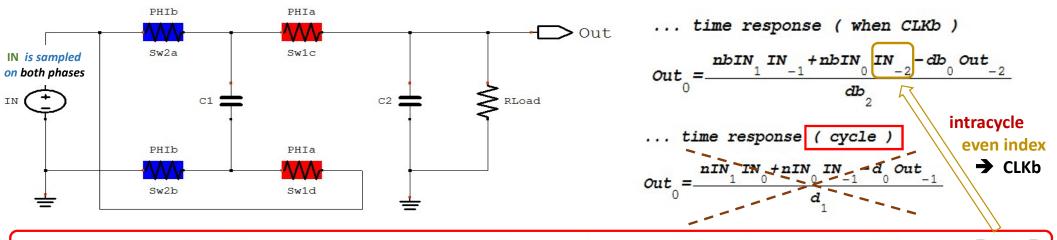


« cycle mode » means « single sampling of the inputs per cycle »

Therefore,

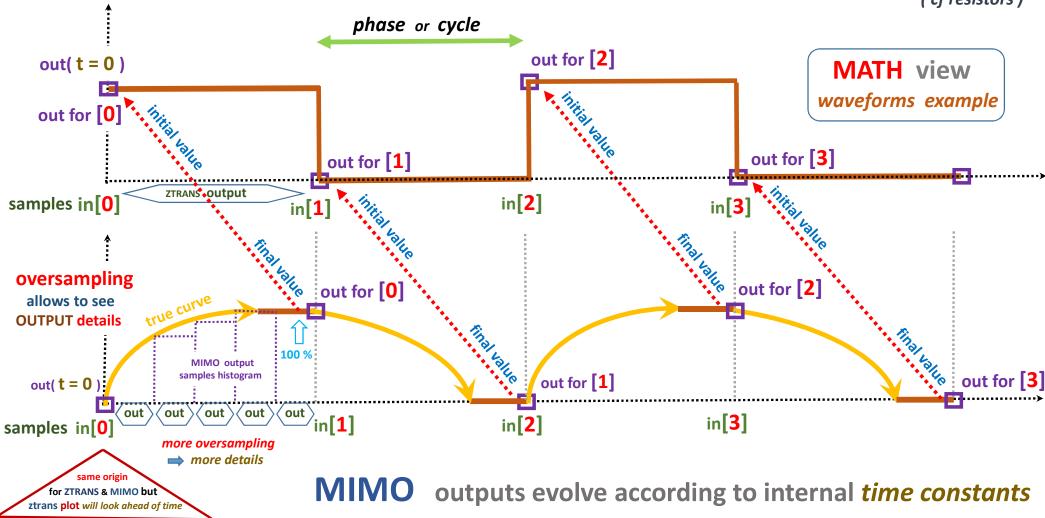
it only makes sens to request « cycle model generation » for physical designs structurally insensitive to intracycle stimuli (otherwise this can also be achieved by externally « latch » the inputs within cycle)





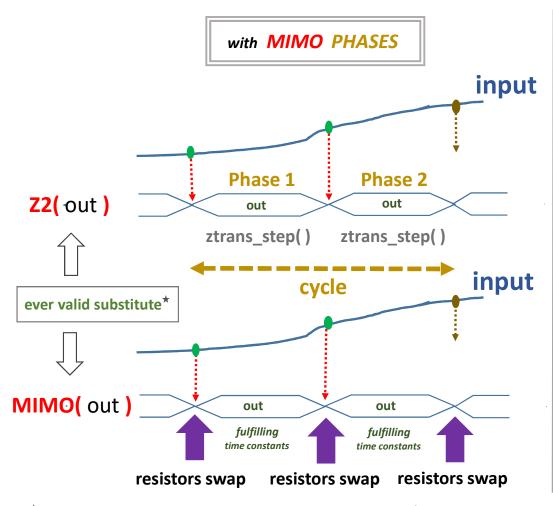
=== NOTE: CYCLE mode will not remain exact in case of intracycle INPUT changes due to CLKb coefficients = $\begin{bmatrix} IN_{-2} \end{bmatrix}$

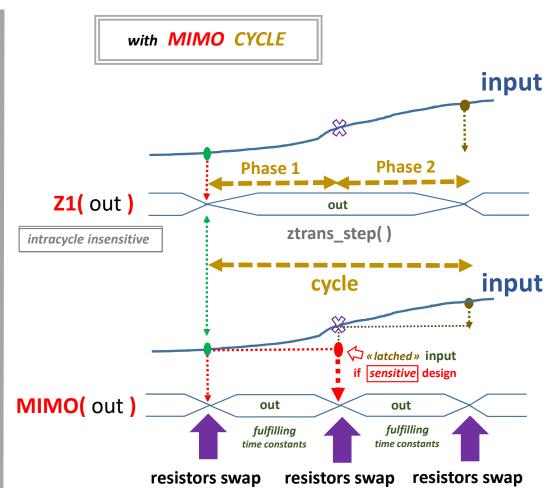
ZTRANS outputs evolve like MIMO would do if the time constants were removed (cf resistors)

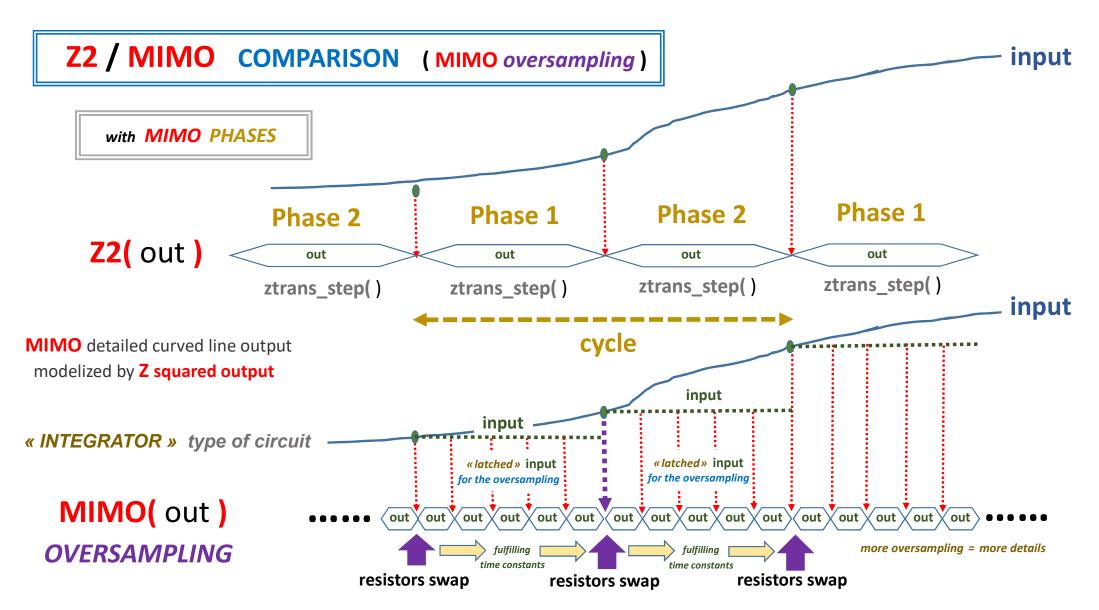


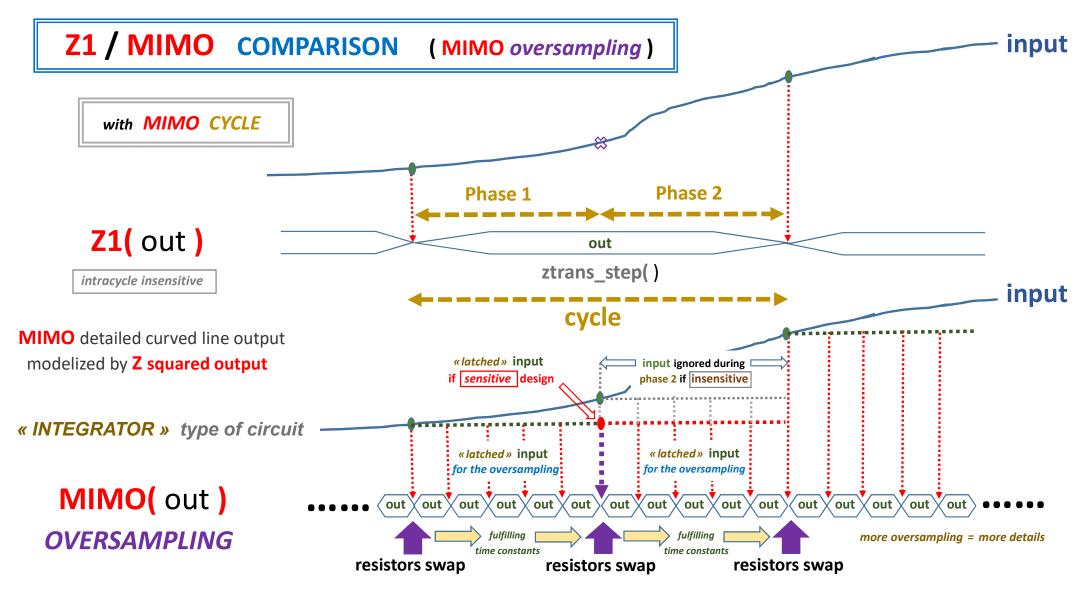
ZTRANS / MIMO COMPARISON

(basic case : cycle = 2 steps)

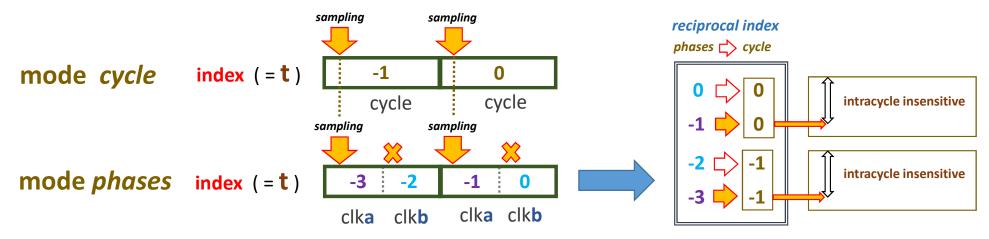






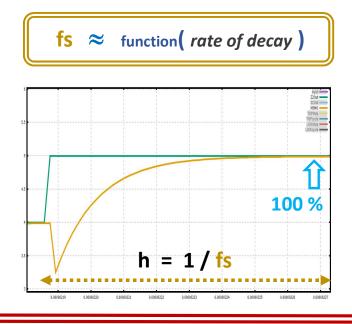


Comparison Z1(Out) versus Z2(Out)

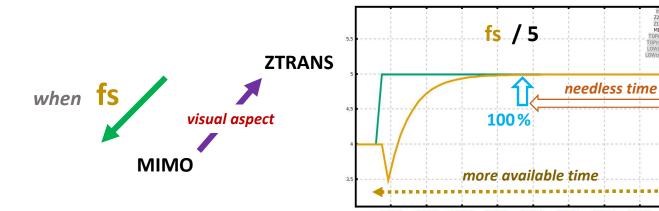


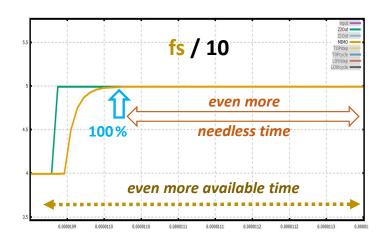
Comparison Z1(Out) versus Z2(Out)

```
Z1(Out)
                                                                                                                                  one sampling and one output value per cycle
                       ... time response ( cycle )
                                          ... time response (when CLKa) irrelevant: cycle unique value masks any clka transient output
                                                                                                                                                                                                                                                                                                                   Z2(Out)
                                        ... time response (when CLKb) equation of clkb output calculates by construct the final [clka clkb] output value
                                        Out =-
                                                                                                                                                                                                                         this expression plotted all along the cycle = Z1(Out)
   intracycle insensitive
                                                                                                                                                                                                                                                                by construction there is no possible Out coefficient
                                                                                                      Offs
                                                                                                                                                                                                    Vin
                                                                                                                                                                                                                            not present in
                                                                  Offs
                                                                                                                                                                     VC2
     internal cycle
                                                                                                                                                                                                              -1 above expression
                                                                                                                                                                                                                                                                clkb is not using clka output as one of its inputs
                                                                                                      Offs
                                                                  Offs
cycle / cycle shift
                                                                                                                    -1
                                                   (nbOffs + nbOffs \ ) \ Offs + (nbVC2 + nbVC2 \ ) \ VC2 + nbOffs \ Offs + nbVC2 \ VC2 + nbVin \ Vin \ -db \ Out \ 2 \ 1 \ 0 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0 \ -1 \ 0
```









Globals

```
%phases = [ PHIa , PHIb ]
%zshort = RDS
%printpoles = 2
k
AV
Vmax
```

```
>>> SARC Z Integrator -> i1 -> POLES
             POLES : matrix(
             [ -1.67226124659431225154548883438110E+04
                                          , +0.000000000000000000000000000000000E+00
rate of decay
            [ -8.01445909870613068342208862304688E+07
                                          [ -5.00499980004099138519040000000000E+23
                                          REAL part
                                                   frequency part
             >>> SARC Z Integrator -> i1 -> POLES
             POLES : matrix(
             [ -1.67226124659431225154548883438110E+04
                                          , +0.0000000000000000000000000000000000E+00
             [ -8.01445909870613068342208862304688E+07
                                          , +0.000000000000000000000000000000000E+00
             [ -5.00499980004099138519040000000000E+23
```

$$e^{-at} = e^{-t/\tau}$$

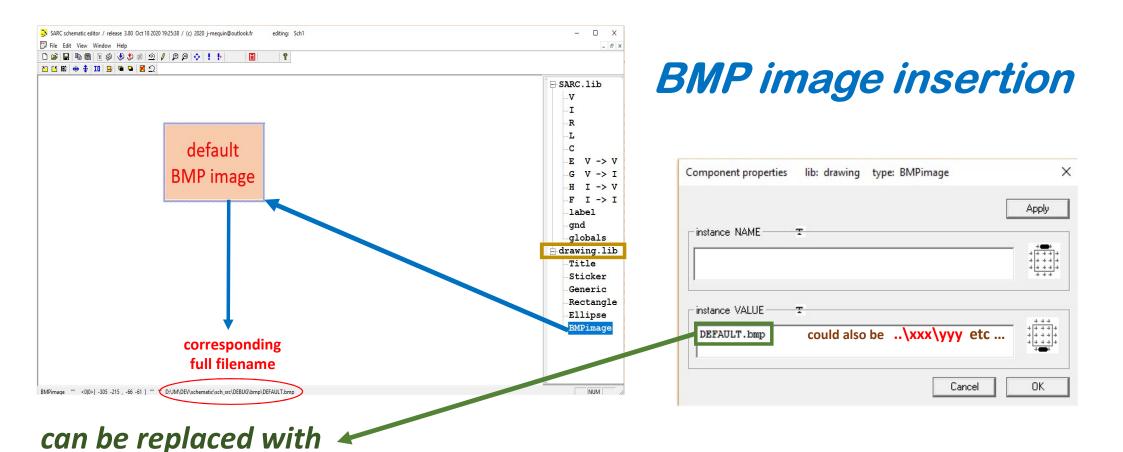
10
$$\tau \implies e^{-1/10} \approx 90\%$$

$$\tau = \frac{1}{a} \approx 35\%$$
 full charge time

$$fs \approx \frac{a}{10}$$
 for 90%

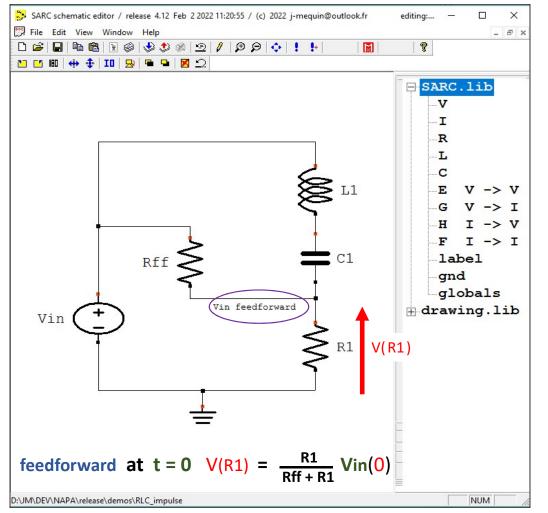
n = -1 / ln(%)

5 is a recommended rule of thumb



- a **filename** that can be found in the **document folder** (document must have been already saved at least once)
- a filename that can be found in the schematic editor installation bmp/folder (= visible by all documents)
- a full filename

impulse response

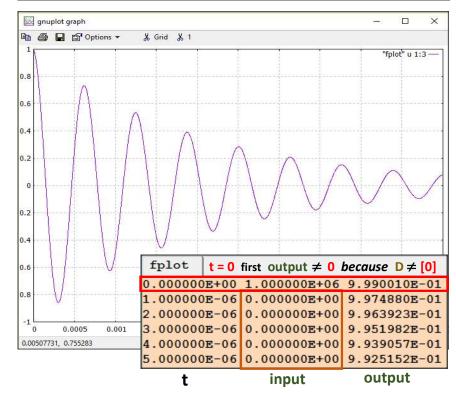


standalone «C» validation model

SARC setup	
INTERN	
OUTPUTS V (R1)	
	execution order 0 GENERATE_XFER(nointern) execution order 0 GENERATE_MIMO
showxfer 🔽 XWISH	
napamode : false ; GENERATE MIMO();	
User code	
Maxima	

```
ANALOG_VIEW: false | MATH
RLC impulse.c
                                                              R1 Rff
#include
                    <stdio.h>
                                                           L1 (Rff + R1)
#include
                 "SARC api.h"
#include "RLC impulse mimo.h"
                                                                        0
                                                              C1
double
                  = 100E-3 ;
                                                             Rff
double
                  = 100E-9 ;
double R1
                  = 100
                                                      B= L1 (Rff +R1)
double
        Rff
                  = 1E5
                                                             0
double
        dirac
                  = 1E6
int
        samples
                                                          R1 Rff
                                                          Rff + R1
int main()
                                                           R1
                                                      D =
                                                                   feedforward
                                                          Rff + R1
         *fgnuplot = fopen( "fplot" , "w" ) ;
  FILE
  double t = 0
  double h = 1 / dirac ;
  double Vin = 0
                          ; double normalize = R1 / L1 ; /* max output */
  pSARC psarc = SARC ALLOC( MIMO RLC impulse() , "i1" ) ;
  double U[ 1 ] ; double *lookup INPUTS[ ] = { &U[0] }
  double Y[1]; double *lookup OUTPUTS[] = { &Y[0] }
                     double
                                *lookup BOM[] = { &L1 , &C1 , &R1 , &Rff } ;
  psarc->lookup h
  psarc->lookup INPUTS = &lookup INPUTS[ 0 ] ;
  psarc->lookup OUTPUTS = &lookup OUTPUTS[ 0 ] ;
  psarc->lookup BOM
                        = &lookup BOM[ 0 ]
  for ( int n = 0 ; n < samples ; n++ )
      if (t == 0) Vin = dirac; dirac (single step)
                        SARC STEP Tminus ( psarc ) ;
      U[ 0 ] = Vin ; SARC STEP Tplus ( psarc ) ;
      fprintf( fgnuplot , "%E %E %E\n" , t , U[0] , Y[0] / normalize ) ;
      t = t + h;
  return 0 ;
```

```
man gnuplot
                                                                   File Plot Expressions Functions General Axes Chart Styles 3D Help
🗪 Replot 😅 Open 🖫 Save 🥕 ChDir 🚔 Print 🎒 PrtSc 👩 Prev 👩 Next 😭 Options 🕶
       GNUPLOT
       Version 5.0 patchlevel 6 last modified 2017-03-18
        Copyright (C) 1986-1993, 1998, 2004, 2007-2017
       Thomas Williams, Colin Kelley and many others
       gnuplot home:
                         http://www.gnuplot.info
        faq, bugs, etc:
                         type "help FAQ"
       immediate help: type "help" (plot window: hit 'h')
Terminal type set to 'windows'
gnuplot> cd "/jm/dev/napa/release/demos/RLC_impulse"
gnuplot> plot "fplot" u 1:3 w 1
gnuplot>
```



APPENDIX

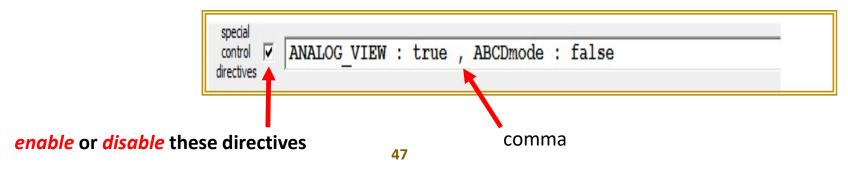
The wxPAUSE() function can be used to force a pause during the execution of a wxMaxima script

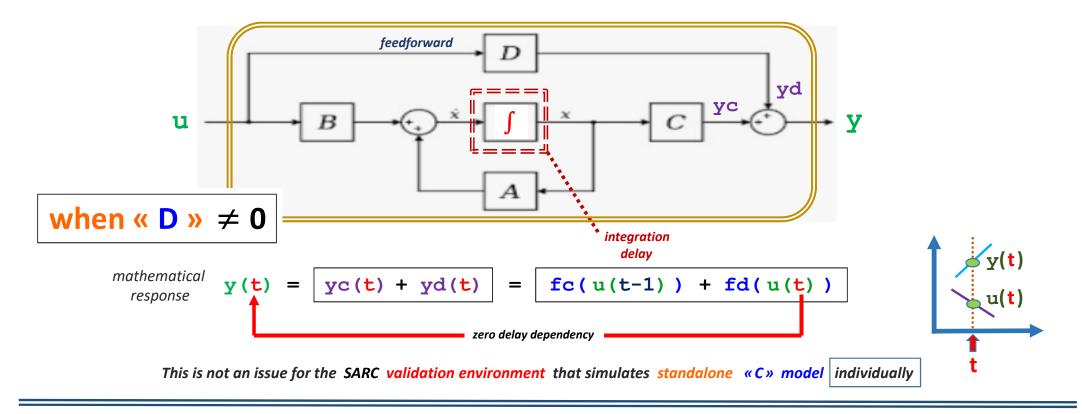
napamode : false ; true / false ANALOG_VIEW : false ; true / false convention : "PSC" ; "PSC" / "GLC" ABCDmode : true ; true / false

default SARC *environment variables*

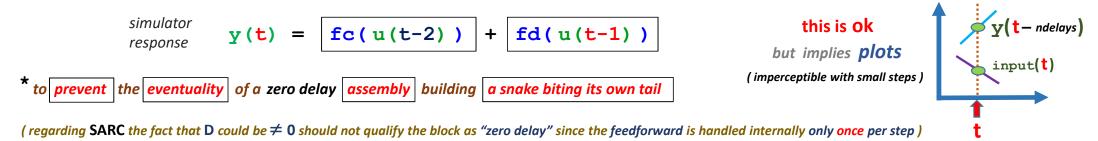
could be redefined in the Maxima initialisation environment file

Of in a **SARC setup** pop up menu





but, independently of SARC, zero delay blocks are almost always delayed* by simulators (including NAPA)

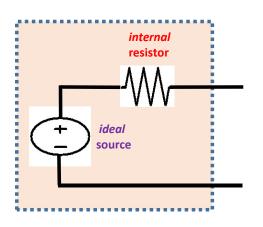


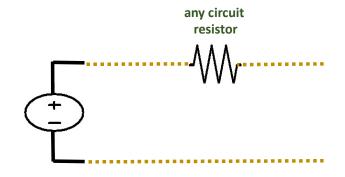
academic physical source

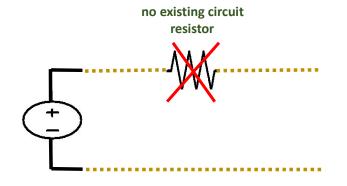
SARC proper source

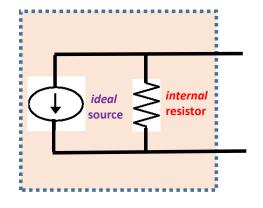
SARC boundless source

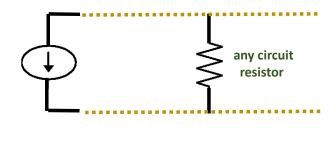
NO RESISTOR NO TIME CONSTANT

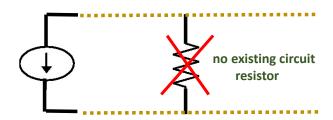


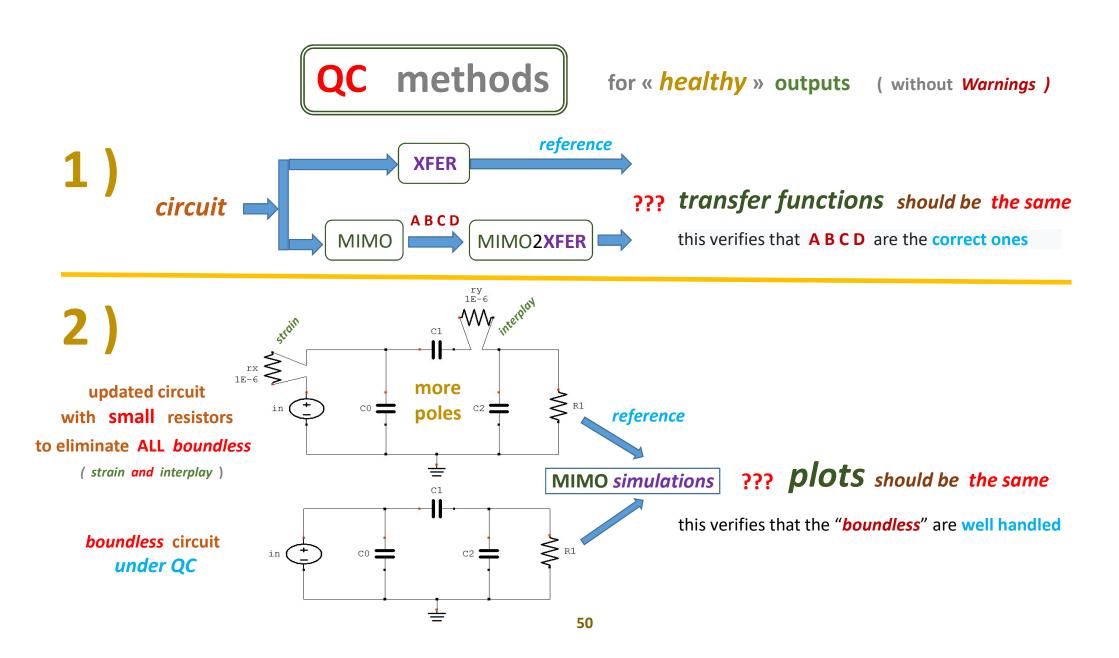




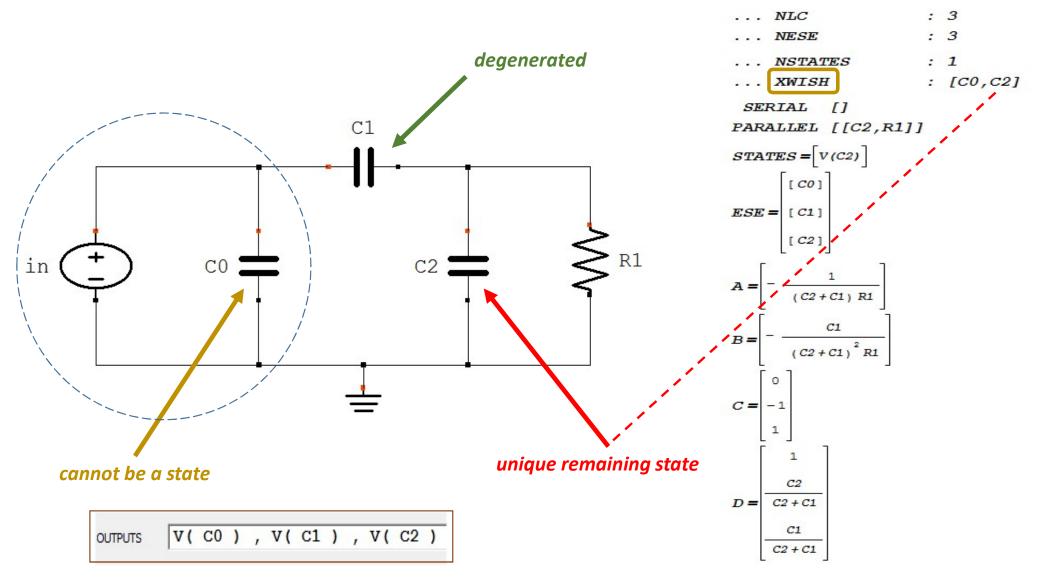


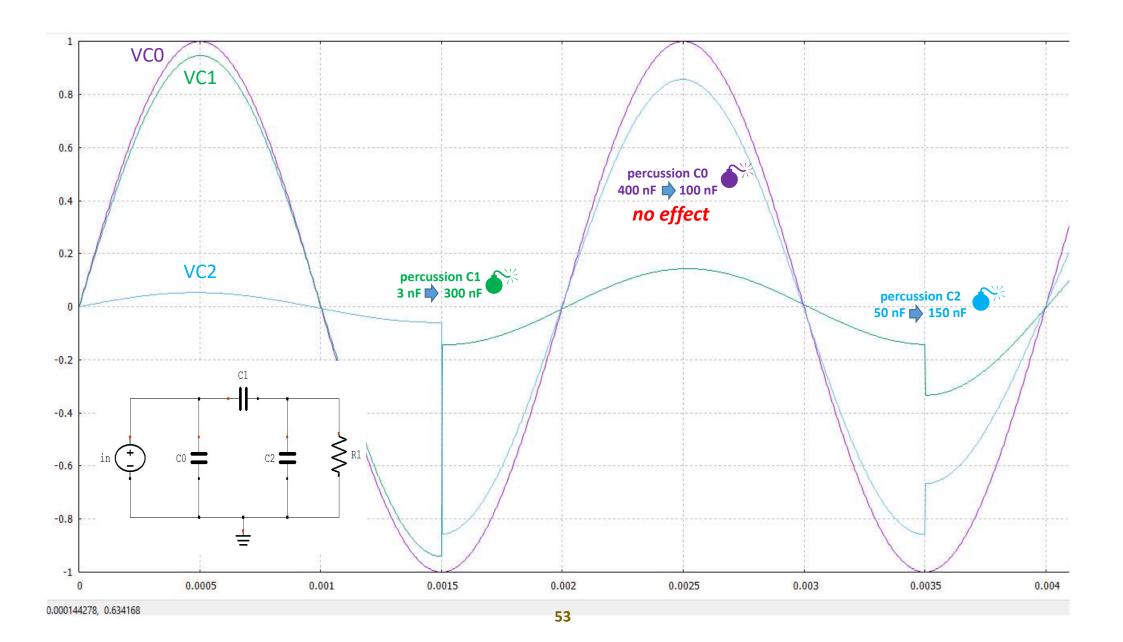




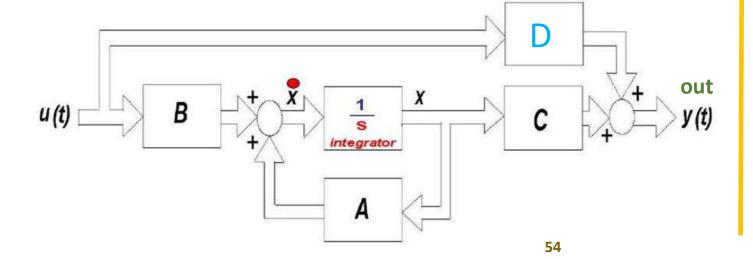


```
MIMO status variables
 NULL
           = DEGEN + AKIN (degenerated states are toxic for circuit state matrix invertibility)
 DEGEN
           "degenerated" states
               * when several C are forming a mesh
                          one C voltage is therefore enslaved to the others ( KVL )
               * when several L have their respective current converging to an ISOLATED node
                          one L current is therefore enslaved to the others ( KCL )
           In those configurations, it should be noted that a voltage source can also play
           the role of a C or a current source the role of a L
 AKIN
           simplest kind of "degenerated" states : parallel C or serial L
                same voltage for C
                                      ( ==>> redundant states )
                  or current for L
                                      ( ==>> redundant states )
 COX
           collinear states resulting from ISOLATED serial C or parallel L
                same current for C
                                      ( I = C dV/dt = C dX/dt ==>> prorated states )
                  or voltage for L (V = L dI/dt = L dX/dt ==>> prorated states)
            thus they cannot be counted as "degenerated" BUT they must be also discarded for
            state matrix invertibility
 U2X
           boundless INPUT sources
                                              ( no R limiting maximum source throughput )
 CS2X
           boundless controlled sources
                                              ( no R limiting maximum source throughput )
 P2X
           PERCUSSION states reciprocal contribution
```





out B T S Integrator A



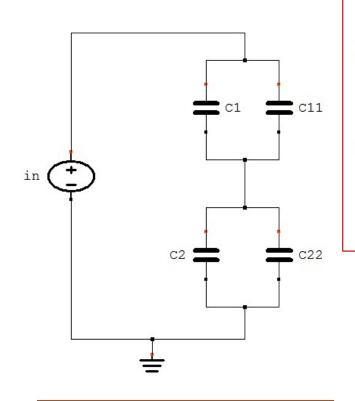
MIMO2XFER

PROPER transfer function

$$D[out] = 0$$

BIPROPER transfer function

$$D[out] \neq 0$$



OUTPUTS

V(C1), V(C2)

GENERATE_XFER

SERIAL []
PARALLEL [[C1,C11],[C2,C22]]

$$1 \quad V(C1)/in = \frac{C22 + C2}{C22 + C2 + C11 + C1}$$

$$2 \quad V(C2)/in = \frac{C11+C1}{C22+C2+C11+C1}$$

no |s| term no pole

GENERATE_MIMO

SERIAL []
PARALLEL [[C1,C11],[C2,C22]]

STATES = none

$$\mathbf{ESE} = \begin{bmatrix} [C1, C11] \\ [C2, C22] \end{bmatrix}$$

$$D = \begin{bmatrix} \frac{C22 + C2}{C22 + C2 + C11 + C1} & \text{no} \\ \frac{C11 + C1}{C22 + C2 + C11 + C1} & \text{on} \end{bmatrix}$$

no state no eigenvalue

only D feedforward matrix

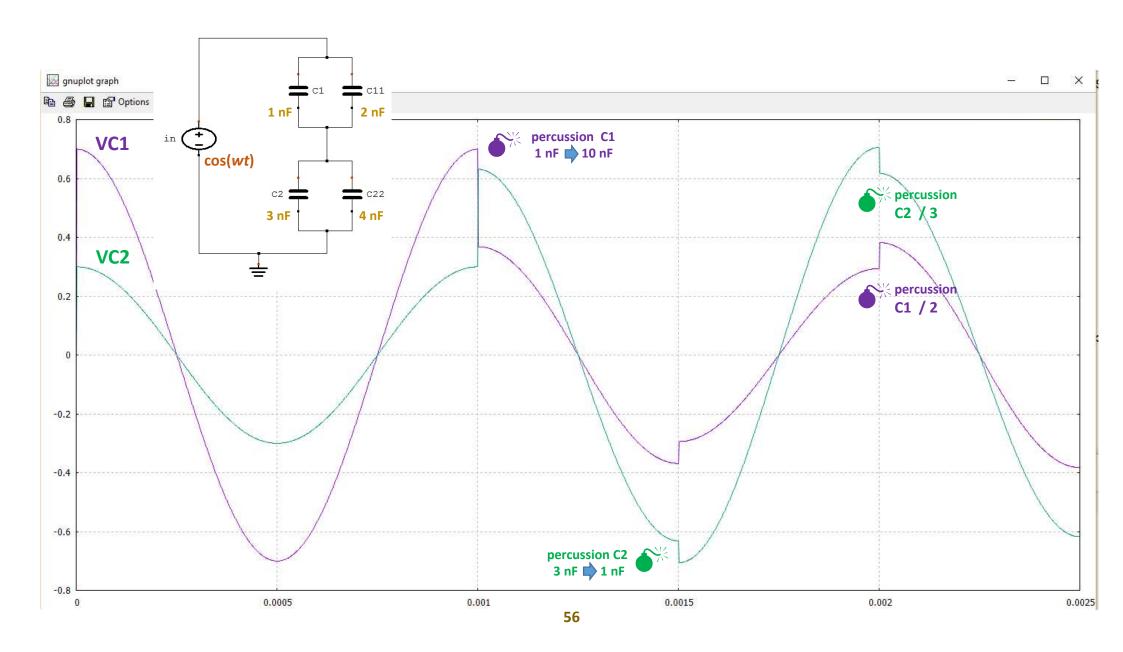
MIMO2XFER XFER =
$$C \left(|s| I - A \right)^{-1} B + D$$

... INPUTS : [in] OMITTED : n/a

... OUTPUTS : [V(C1), V(C2)]

$$1 \quad V(C1)/in = \frac{C22 + C2}{C22 + C2 + C11 + C1}$$

$$2 \quad V(C2)/in = \frac{C11+C1}{C22+C2+C11+C1}$$

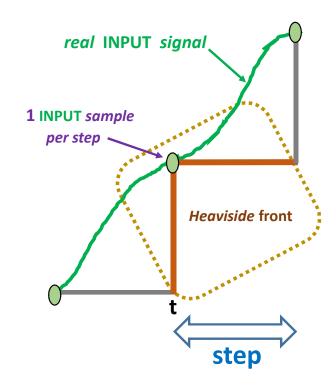


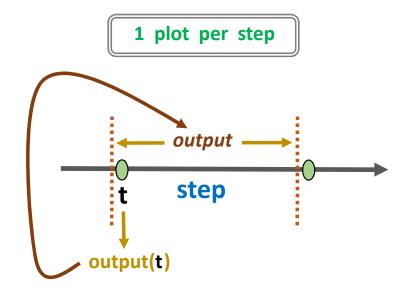
MATH/ANALOG VIEW

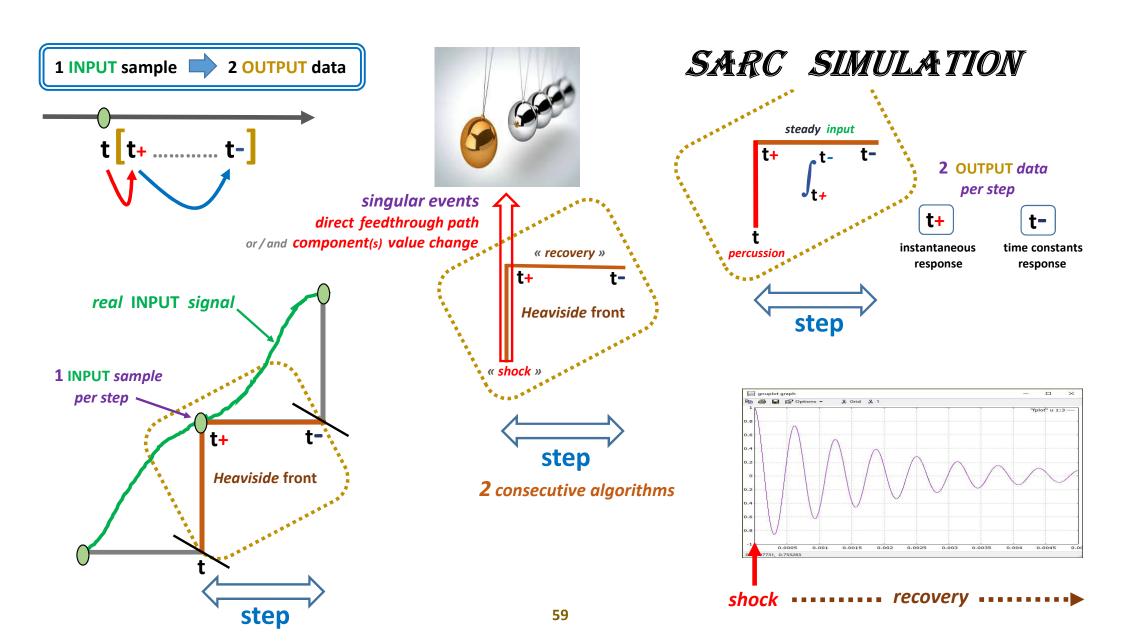
CLASSICAL SIMULATION

(almost always not accepting "on fly" component value change)

1 INPUT sample | 1 OUTPUT data



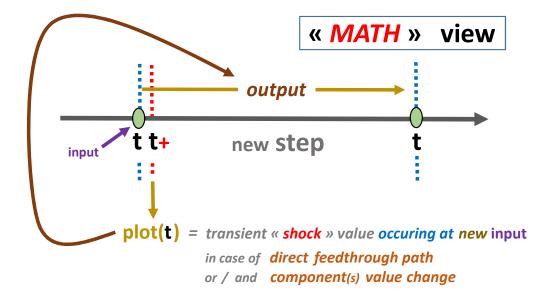




SARC SIMULATION

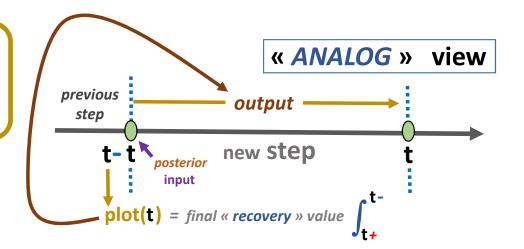
1 plot per step

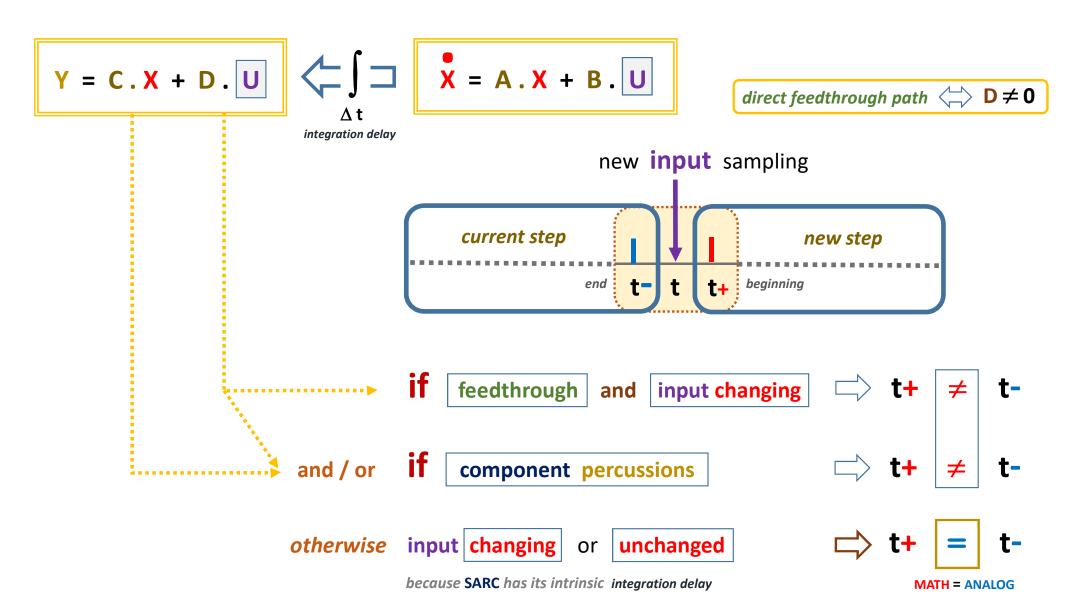
The VIEW is a user choice that does not affect the overall accuracy of the simulation which remains based on the consecutive processing of both t+ and t-



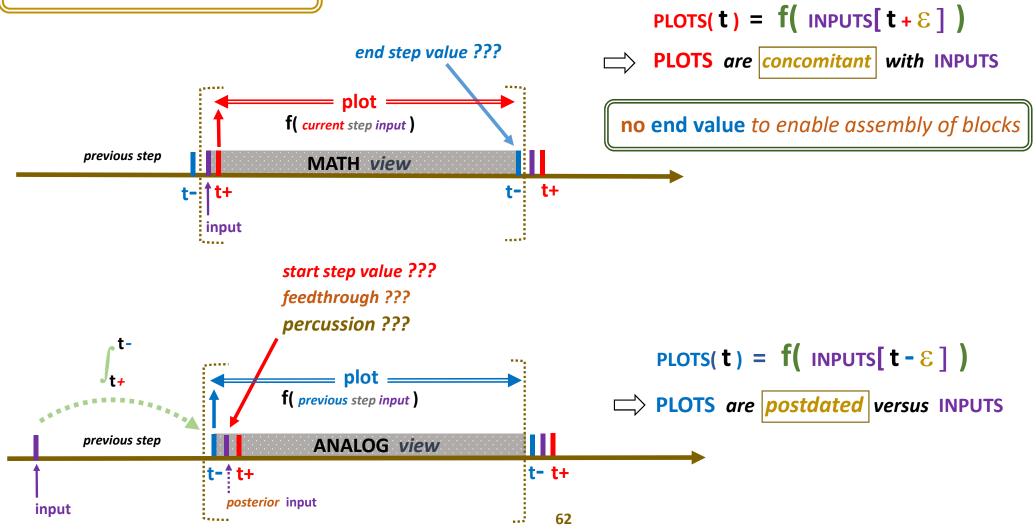
However, this is only true when the *entire simulated circuit* has been **edited** as a *single SARC schematic*

ANALOG view is required to simulate assembly of blocks





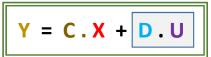
SARC uncertainty

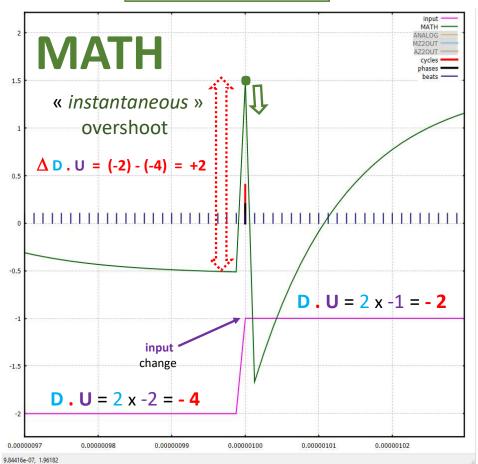


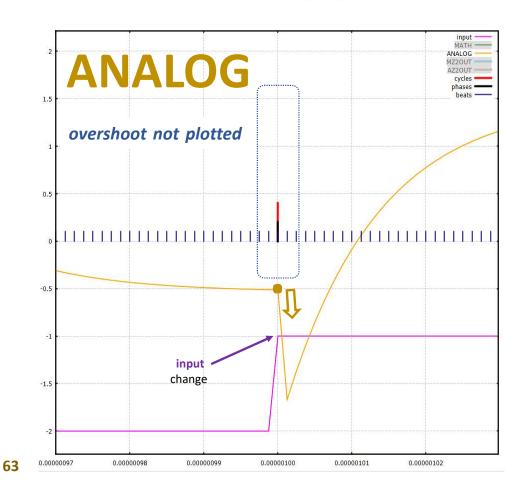
SARC uncertainty

direct feedthrough path matrix
$$D = \begin{bmatrix} 2 & 0 \end{bmatrix} \neq 0$$

inputs vector $U = \begin{bmatrix} -2 \\ 0 \end{bmatrix}$ followed by $\begin{bmatrix} -1 \\ 0 \end{bmatrix}$





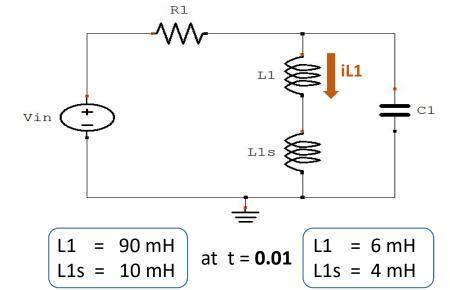


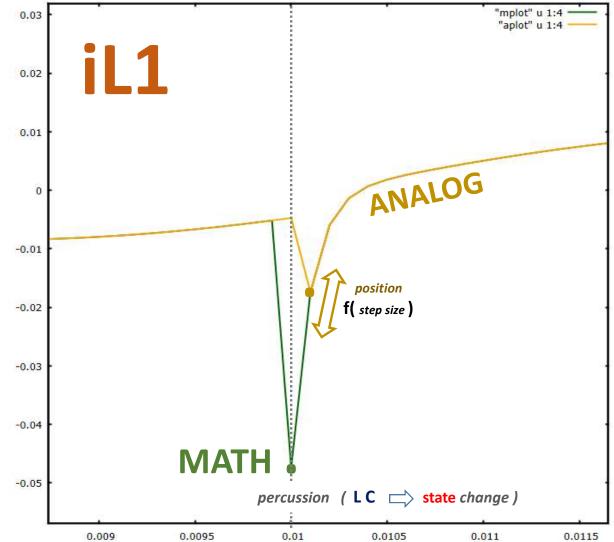
SARC uncertainty

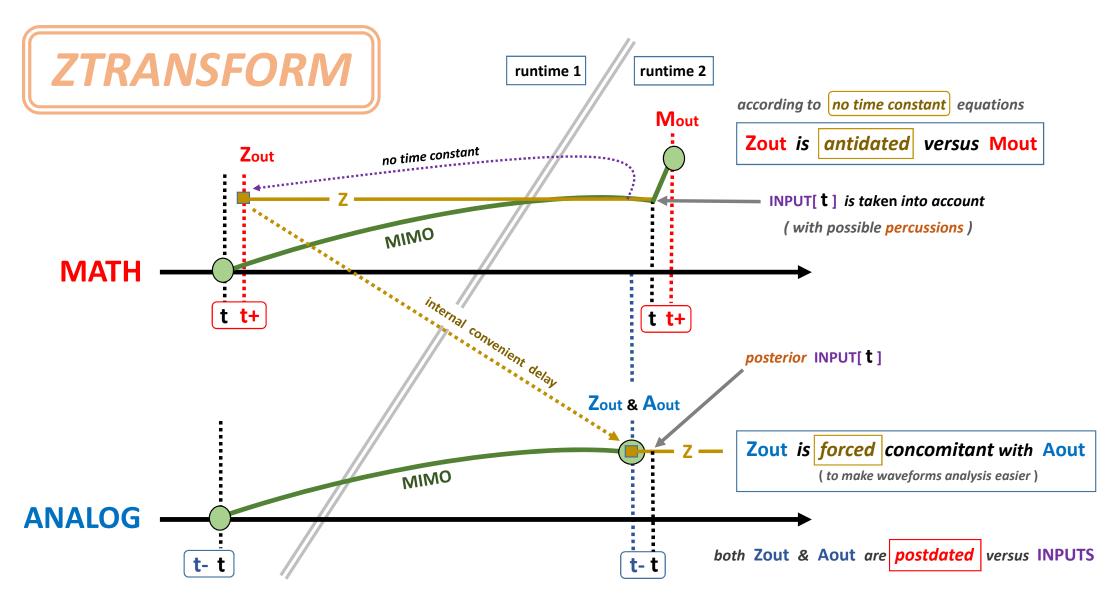
$$Y = C \cdot X + D \cdot U$$

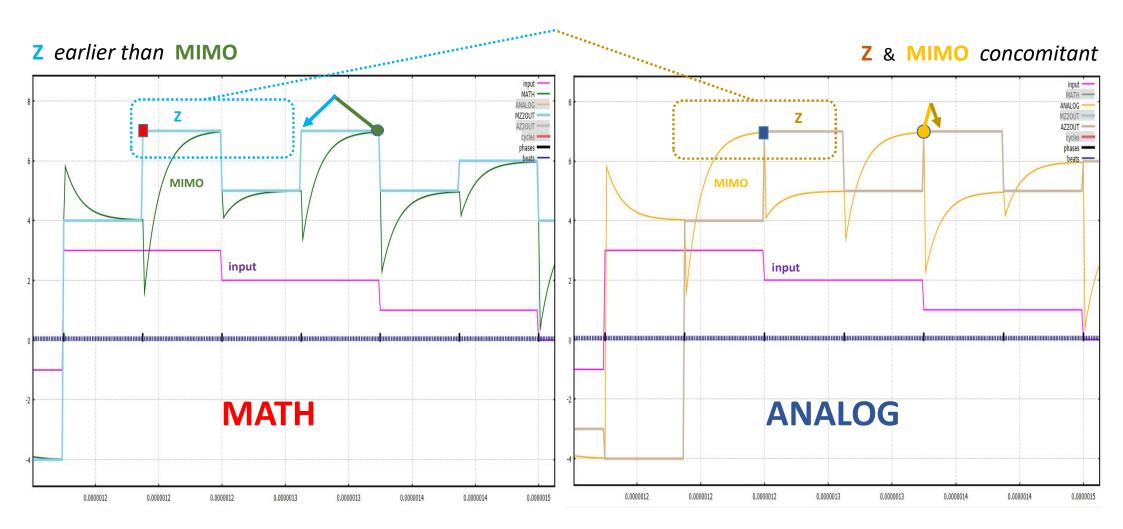
component value change

could affect **C** or/and **D** (and **X** if **LC**)



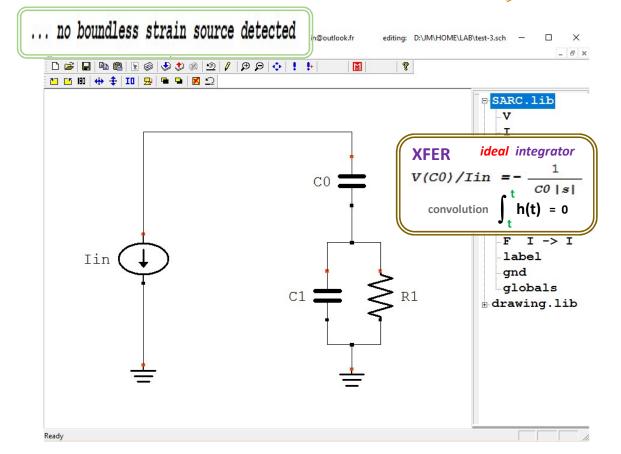






runtime WARNINGS

Y = 0



conversion MIMO > XFER

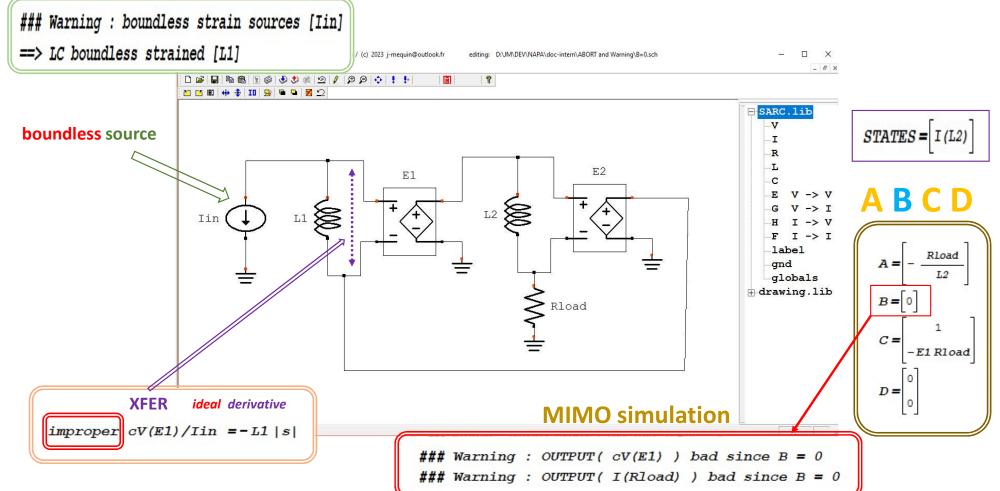
1 warning
$$V(C0)/I$$
in =0
2 proper $V(C1)/I$ in =- $\frac{R1}{C1R1|s|+1}$

MIMO simulation

Warning : OUTPUT(V(C0)) = 0

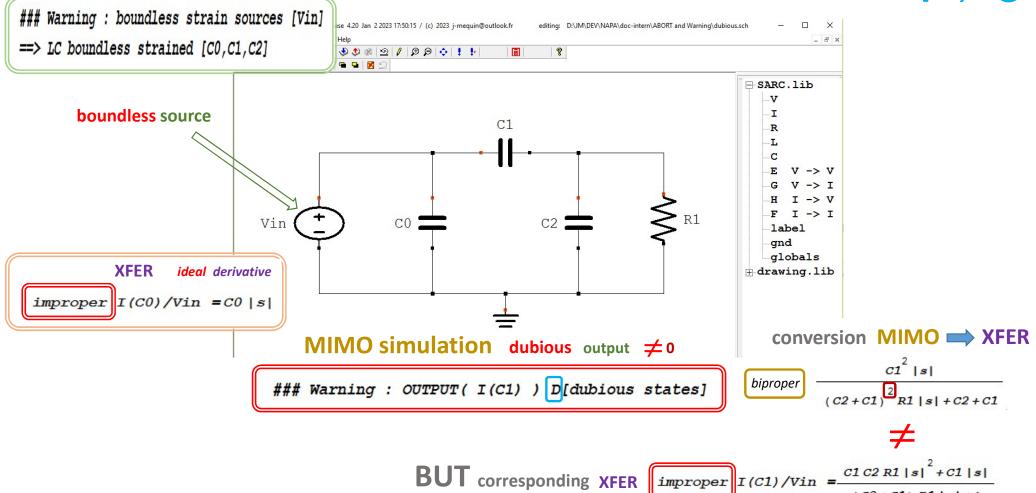
please note: the fact that V(C0) is flagged does not prevent V(C1) to be correct

B = 0

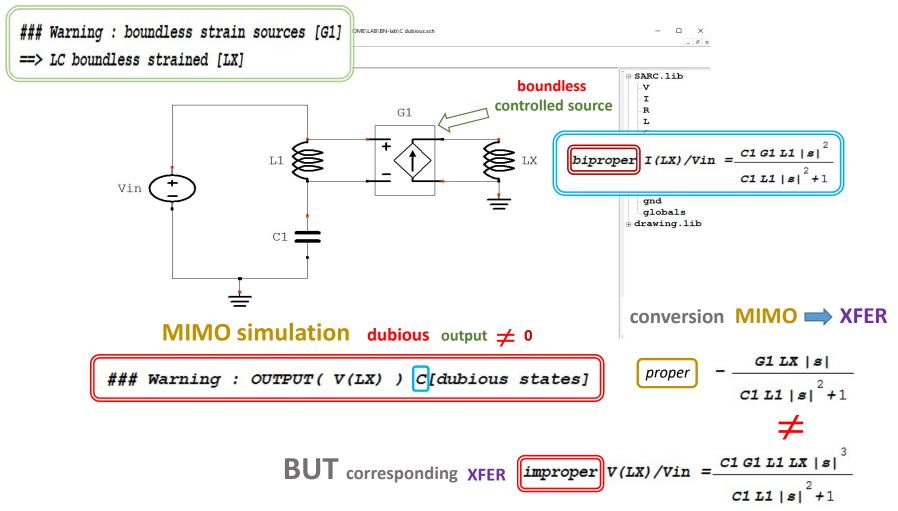




(C2+C1) R1 |s|+1

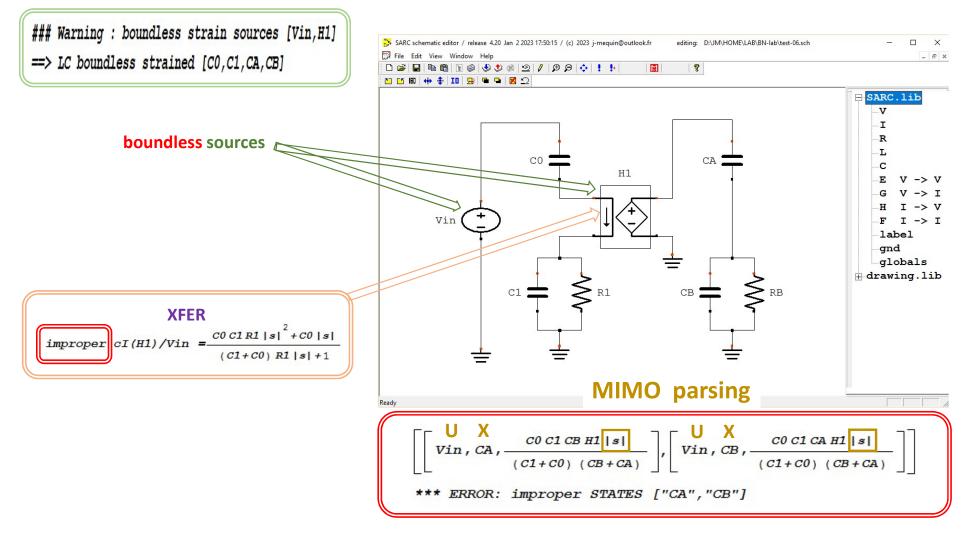


Y ≠ **0**



BAD SCHEMATIC

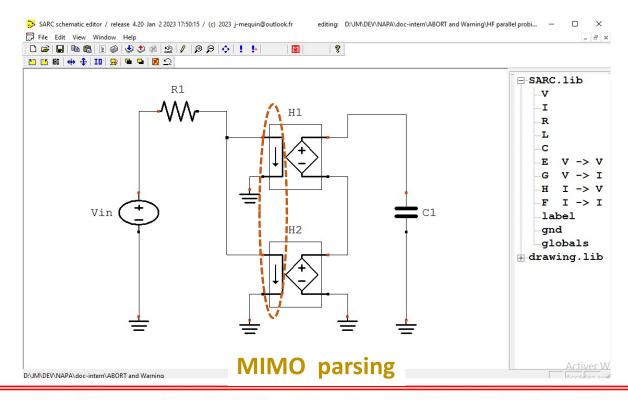
ABORT



BAD SCHEMATIC

ABORT

schematic may look good but may not be simulable



*** ERROR: parallel CURRENT probing for CONTROL side [["H1","H2"]]